S8005

Version 1.00

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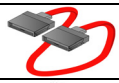
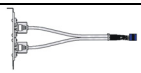
Before you begin…

Check the box contents!

The retail motherboard package should contain the following:

	S8005 Motherboard
	**6 x Serial ATA Cable**
	1 x SAS Cable (for S8005WAGM2NR SKU only)
	**USB2.0 Cable**
	S8005 User's manual
	S8005 Quick reference guide
	TYAN® Driver CD
	I/O shield
	Retention Module

Chapter 1: Instruction

1.1 Congratulations

You have purchased one of the most powerful TYAN® S8005 motherboard. Based on AMD® chipsets, the S8005 is designed to support AMD® AM3 Socket processors and four DDR3 un-buffered, ECC DDR3 1333/1066/800/667MHz memory, providing a rich feature set and incredible performance. Leveraging advanced technology from AMD®, the S8005 is capable of offering scalable 32 and 64-bit computing, high-bandwidth memory design, and lightning-fast PCI-E bus implementation.

The S8005 not only empowers you in today's demanding IT environment but also offers a smooth path for future application usage. All of this provides S8005 the power and flexibility to meet the needs of nowadays application.

Remember to visit TYAN®'s website at http://www.tyan.com. There you can find information on all of TYAN®'s products with FAQs, online manuals and BIOS upgrades.

1.2 Hardware Specifications

Processor	**Supported CPU Series**	AMD Opteron™ 1000 series Quad Core CPU's
	Socket Type / QTY	uPGA 941-pin AM3 socket / (1)
	Average CPU Power (ACP) wattage	75W
	System Bus	HT3 @ (17.6GB/s) 4.4GT/s
Chipset	**IOH / SB**	AMD SR5670(SR5650)/ SP5100
	Super I/O	Winbond W83627DHG-P
Memory	**Supported DIMM Qty**	(4) DIMM slots
	DIMM Type / Speed	Unbuffered ECC DDR3 667/800/1066/1333*
	Capacity	Up to 16GB
	Memory channel	Dual Channel
	Memory voltage	1.5V
	Note: 1333MHz limit 1/Channel, if 2/channel max speed will be 1066MHz	
Expansion Slots	**PCI-E**	(1) PCI-E x16 slot (1) PCI-E x8 slot (for SR5670 SKU)
Recommended Barebones / Chassis	**1U Barebones**	GT14-B8005
LAN	**Port QTY**	(2)
	Controller	Intel 82574L

Storage	**SATA**	**Connector**	(6) SATA
		Controller	SP5100
		Speed	3.0 Gb/s
		Note: SATA CD-ROM must be connected to port 5 or 6 under AHCI mode.	
	SAS	**Connector**	(4) SAS
		Controller	Marvell 88SE6440
		Speed	3.0 Gb/s
		RAID	RAID 0/1/10/5
	IDE	**Channel**	Single channel
		Controller	SP5100
Graphic	**Connector type**		D-Sub 15-pin
	Resolution		1600x1200 @60 Hz 16bpp
	Chipset		Aspeed AST2050
Audio	**Chipset**		Realtek ALC262
	Feature		High Definition Audio
Input /Output	**USB**		(7) USB2.0 ports (4 at rear, 2 via cable, 1 vertical)
	COM		(2) ports (1 at rear, 1 via cable)
	PS/2		(1) PS/2 connector
	SAS		(4) SAS connectors (for SAS SKU only)
	VGA		(1) D-Sub 15-pin VGA port
	Audio		(1) 2x5-pin front panel audio header (1) 3 port rear audio jack (Line-in/Line-out/Mic)
	RJ-45		(2) GbE ports
	LCM		(1) 3x2-pin LCD module header
	Power		EPS12V 24-pin + 8-pin power connectors
	Front Panel		(1) 2x12-pin TYAN front panel header (TYAN-SSI)
	IPMB		(1) 1x4-pin header
	IDE		(1) IDE connector
	SATA		(6) SATA-II connectors
System Monitoring	**Chipset**		Winbond W83627DHG-P
	Voltage		Monitors voltage for CPU, memory, chipset & power supply
	Fan		Total (4) 4-pin headers
	Temperature		Monitors temperature for CPU & system environment
	LED		Fan fail LED indicator / Over temperature warning indicator / Fan & PSU fail LED indicator
	Others		Chassis intrusion detection / Watchdog timer support
BIOS	**Brand / ROM size**		AMI / 2MB
	Feature		Plug and Play (PnP) /PCI2.3 /WfM2.0 /SMBIOS2.3 /PXE boot / ACPI 2.0 power management /Power on mode after power

			recovery / User-configurable H/W monitoring / Auto-configurable of hard disk types / Multiple boot options
Form Factor	**Form Factor**		ATX
	Board Dimension		9.6" x 12" (244mm x 305mm)
Operating System	**OS supported list**		Please refer to our web site for the OS supported list.
Regulation	**FCC (DoC)**		Class B
	CE (DoC)		Yes
Operating Environment	**Operating Temp.**		10° C ~ 35° C (50° F~ 95° F)
	Non-operating Temp.		- 40° C ~ 70° C (-40° F ~ 158° F)
	In/Non-operating Humidity		90%, non-condensing at 35° C
RoHS	**RoHS 6/6 Complaint**		Yes
Package Contains	**Motherboard**		(1) S8005 Motherboard
	Manual		(1) User's manual / (1) Quick Ref. Guide
	Installation CD		(1) TYAN installation CD
	I/O Shield		(1) I/O Shield
	Cable	**SATA**	(6) SATA signal cables / (2) SAS signal cables
		USB	(1) USB bracket cable
Optional accessories for future upgrade	**Riser Card**		M2061, PCI-E x8 to PCI-X 1U riser card (left) M2083-RS, PCI-E 1U riser card (left)

1.3 AST2050 Application

Please visit the TYAN Web Site at **http://www.tyan.com** to download the latest AST2050 User's Guide.

Chapter 2: Board Installation

You are now ready to install your motherboard.

How to install our products right… the first time

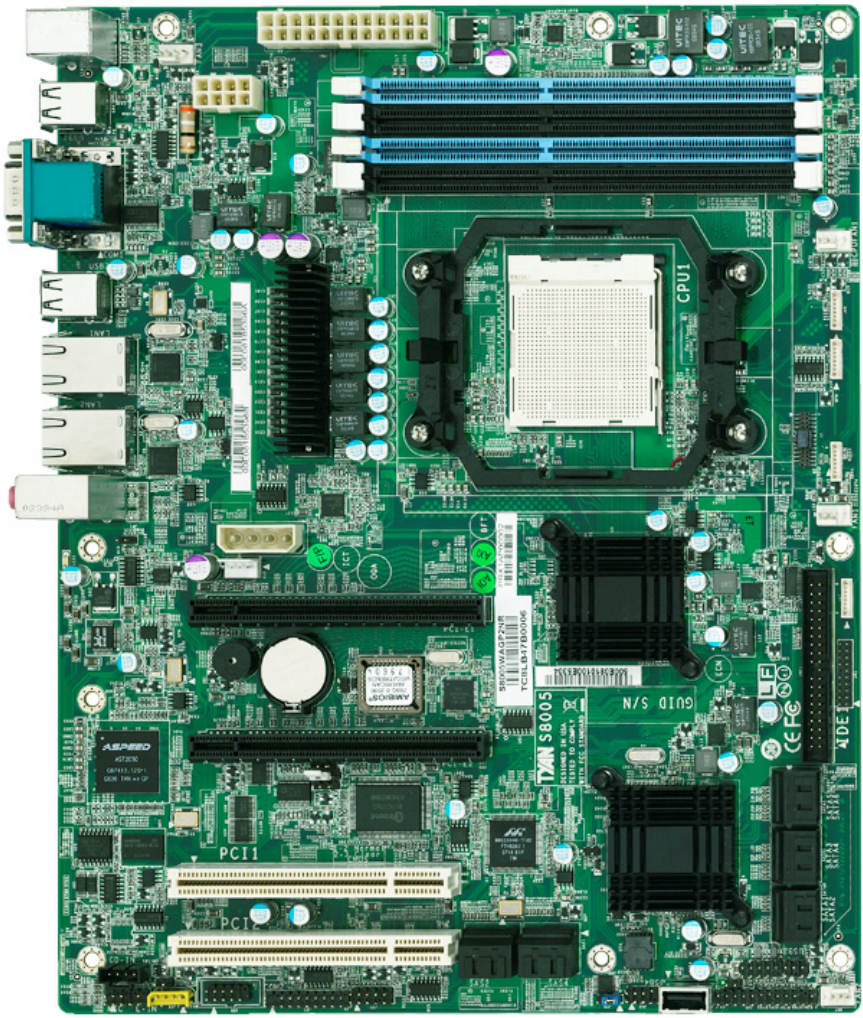
The first thing you should do is reading this user's manual. It contains important information that will make configuration and setup much easier. Here are some precautions you should take when installing your motherboard:

(1) Ground yourself properly before removing your motherboard from the antistatic bag. Unplug the power from your computer power supply and then touch a safely grounded object to release static charge (i.e. power supply case). For the safest conditions, MiTAC recommends wearing a static safety wrist strap.
(2) Hold the motherboard by its edges and do not touch the bottom of the board, or flex the board in any way.
(3) Avoid touching the motherboard components, IC chips, connectors, memory modules, and leads.
(4) Place the motherboard on a grounded antistatic surface or on the antistatic bag that the board was shipped in.
(5) Inspect the board for damage.

The following pages include details on how to install your motherboard into your chassis, as well as installing the processor, memory, disk drives and cables.

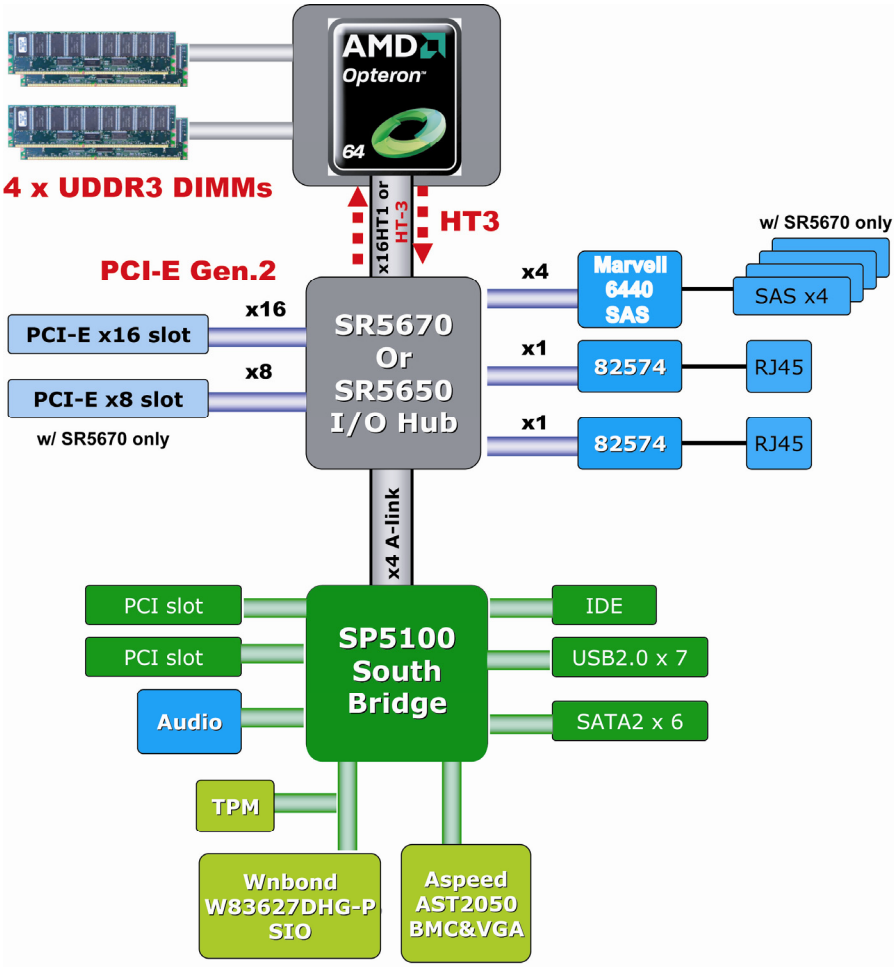
NOTE: Do not apply power to the board if it has been damaged.

2.1 Board Image



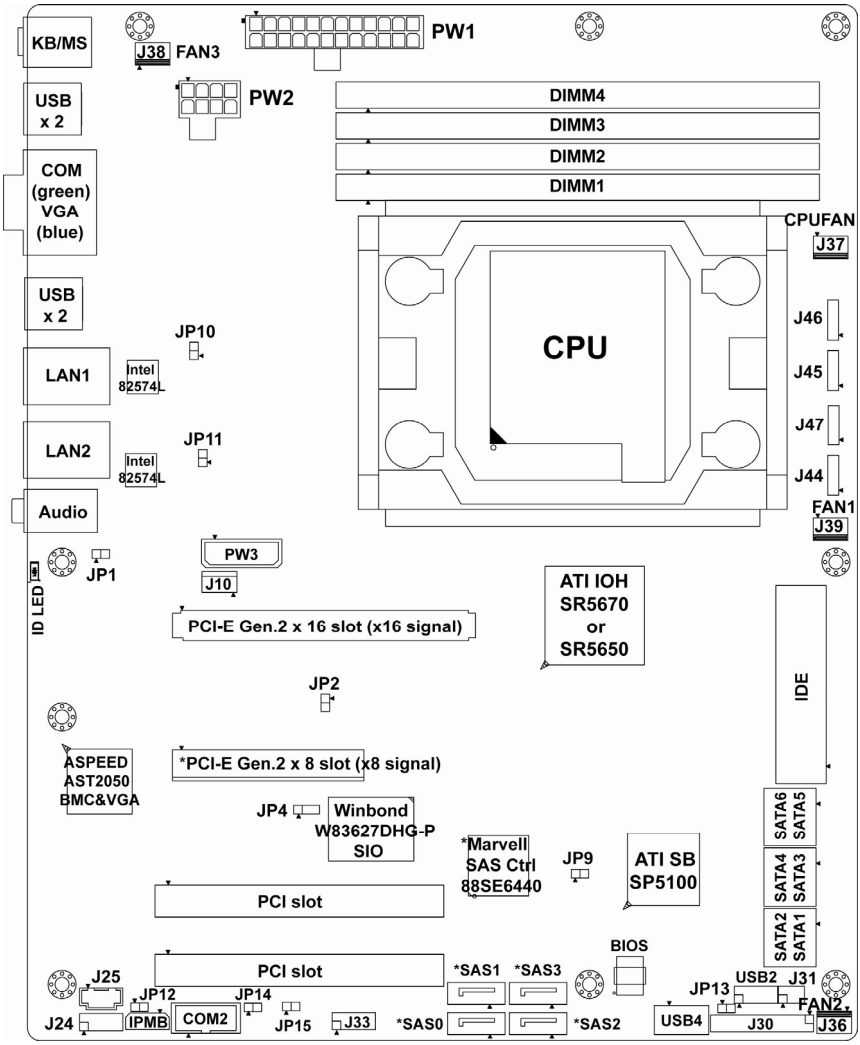
This picture is representative of the latest board revision available by the time of publishing. The board you receive may not look exactly like the above picture.

2.2 Block Diagram



S8005 Block Diagram

2.3 Board Parts, Jumpers and Connectors



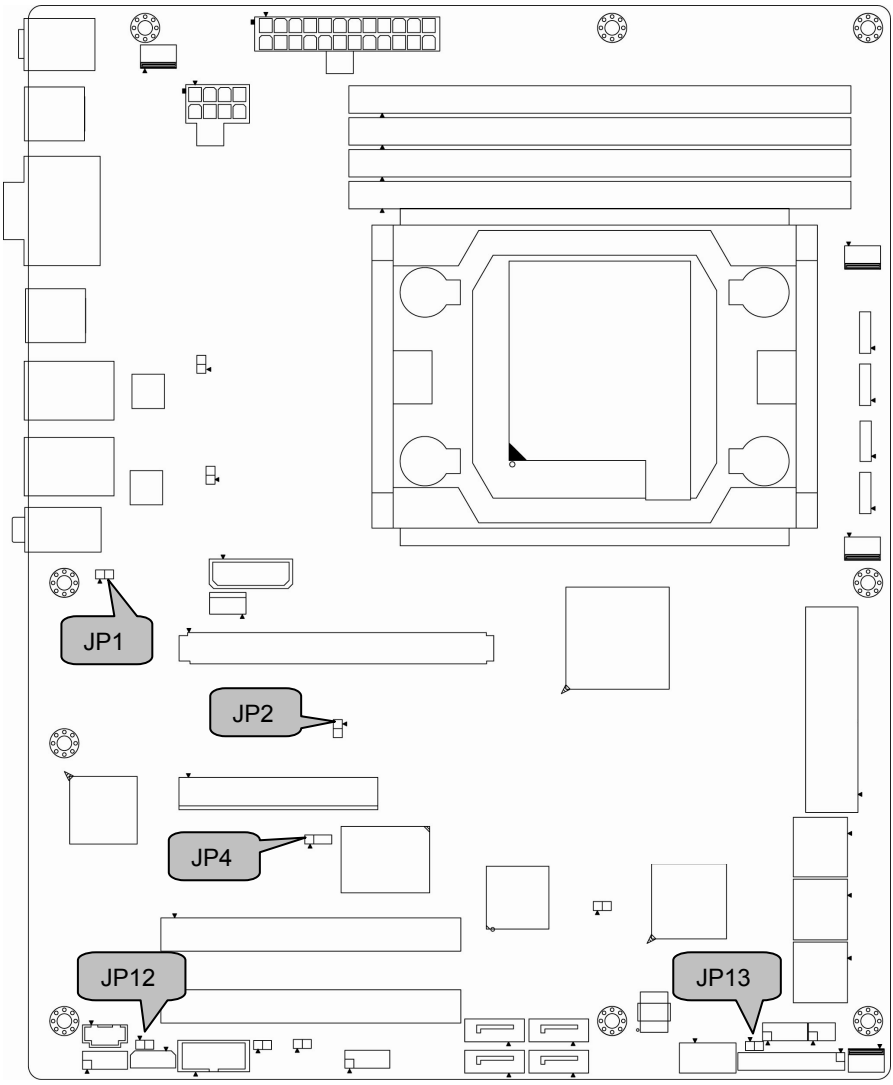
This diagram is representative of the latest board revision available at the time of publishing. The board you receive may not look exactly like the above diagram. For the latest board revision, please visit our web site at www.tyan.com.

Jumpers & Connectors

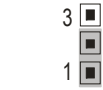
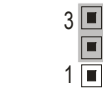
Jumper/Connector	Function
JP1	BMC Reset Jumper
JP2	TPM Reset Jumper
JP4	Clear CMOS Jumper
JP9	Onboard SAS Jumper
JP10/JP11	LAN1/LAN2 Jumper
JP12	WDT Jumper
JP13	Chassis Intrusion Header
JP15	Onboard VGA Jumper
J22	IPMB Connector
J24	Audio Front Panel Header
J25	CD-IN Header
J26	COM2 Connector
J30	TYAN-SSI Front Panel Header
J31	LCM Module Header (Barebones use only)
J44/J45/J46/J47	8-pin FAN Headers (barebones use only)
CPUFAN/FAN1/FAN2/FAN3	4-pin FAN Headers
PW1	24-pin EPS 12V Main Power Connector
PW2	8-pin EPS 12V Power Connector
PW3	4-pin Power Connector for PCI-E add-on card
USB2	USB Front Panel Header
USB4	Type-A Vertical USB Connector

Jumper Legend

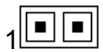
	OPEN - Jumper OFF	Without jumper cover
	CLOSED - Jumper ON	With jumper cover


JP1
JP2
JP4
JP12
JP13

JP4: Clear CMOS Jumper

 **Normal (Default)**	Use this jumper when you forgot your system/setup password or need to clear system BIOS setting. How to clear the CMOS data - Power off system and **disconnect power supply from AC source** - Use jumper cap to close Pin_2 and 3 for several seconds to Clear CMOS - Replace jumper cap to close Pin_1 and 2 Reconnect power supply to AC source Power on system
 **Clear**	

JP13: Chassis Intrusion Header

	**Pin**	**1**	**2**	The Chassis Intrusion Header provides chassis intrusion-monitoring function. **Note:** For use with chassis that support this feature.
	Signal	Intruder#	GND	

JP12: WDT Jumper

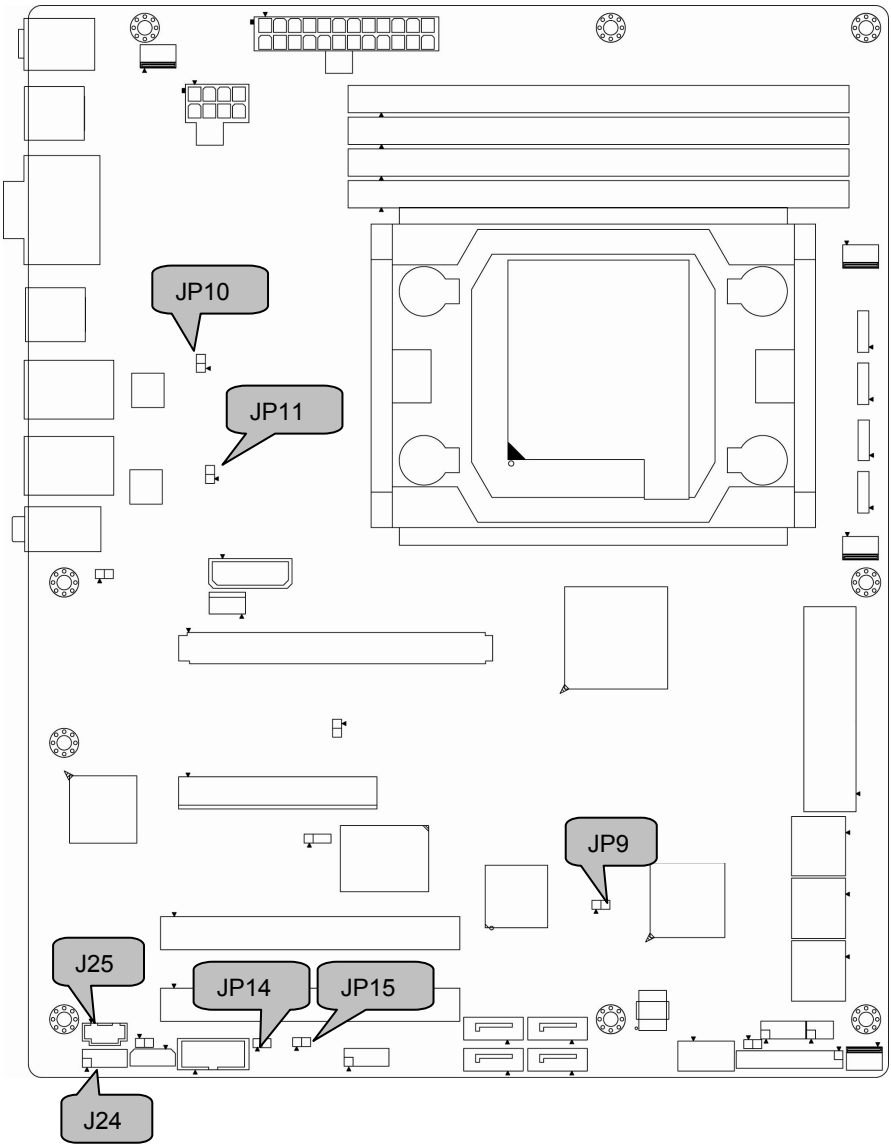
	Open: WDT RESET System (**Default**)
	Closed: WDT Trigger NMI

JP1: BMC Reset Jumper

	Open: Normal (**Default**)
	Closed: BMC RESET

JP2: TPM Reset Jumper

	Open: Normal (**Default**)
	Closed: TPM RESET


JP10
JP11
JP9
J25
JP14
JP15
J24

JP9: Onboard SAS Disable Jumper

	Open: Normal (**Default**)
	Closed: Disable onboard SAS function

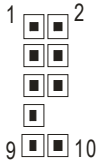
JP10 / JP11: LAN1 / LAN2 Disable Jumper

	Open: Normal (**Default**)
	Closed: Disable LAN1 / LAN2 function

JP15: Onboard VGA Disable Jumper

	Open: Normal (**Default**)
	Closed: Disable onboard VGA function

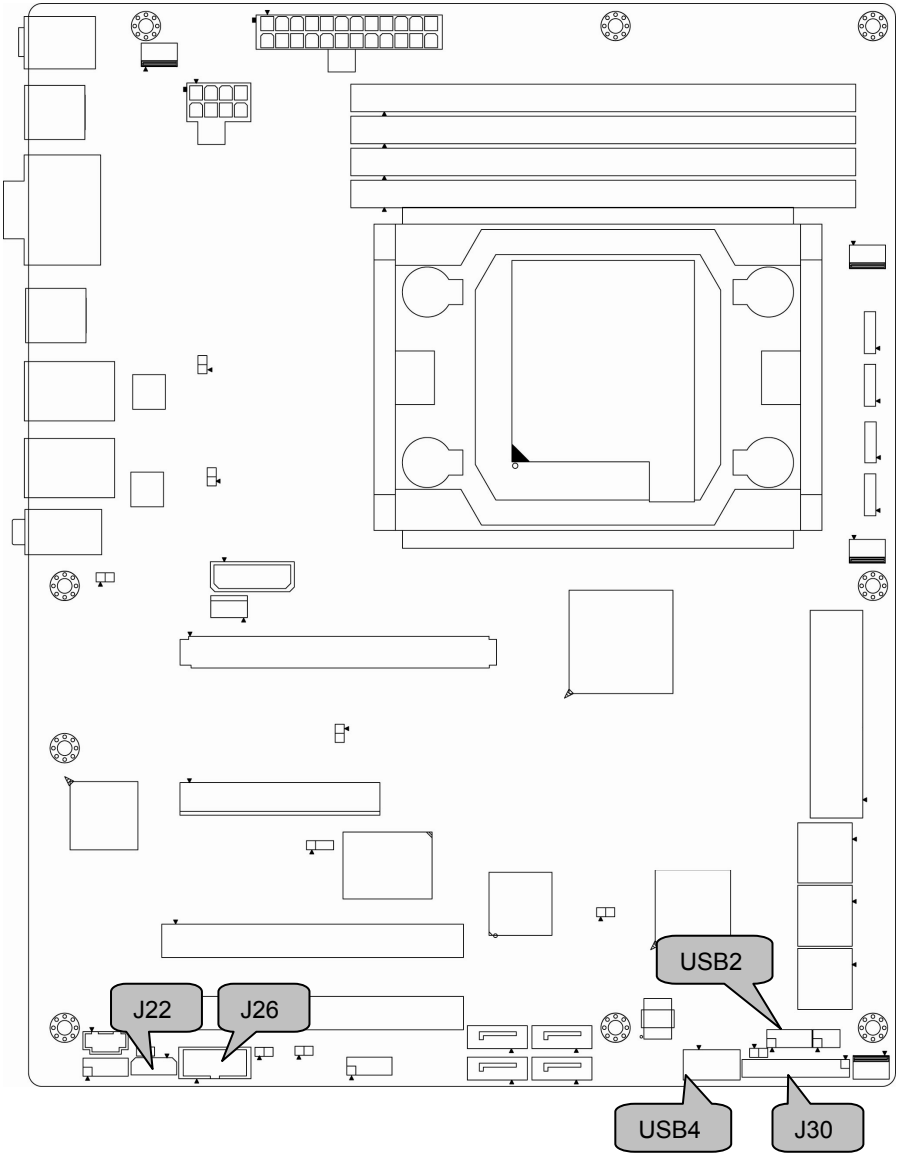
J24: Audio Front Panel Header



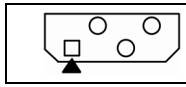
Signal	Pin	Pin	Signal
MIC_L	1	2	GND
MIC_R	3	4	NC
LINE-OUT_R	5	6	MIC_SENSE_RETURN
SENSE_SEND	7	8	KEY
LINE-OUT_L	9	10	LINEOUT_SENSE_RETURN

J25: CD_IN Header

Pin	1	2	3	4
Signal	CD_R	CD_GND	CD_GND	CD_L

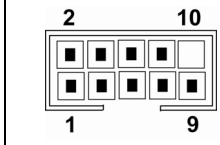

USB2
J22
J26
USB4
J30

J22: IPMB Connector



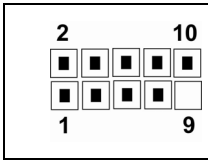
Pin	1	2	3	4
Signal	IPMB DATA	GND	IPMB Clock	NC

J26: COM2 Connector



Signal	Pin	Pin	Signal
DCD	1	2	DSR
RXD	3	4	RTS
TXD	5	6	CTS
DTR	7	8	RI
GND	9	10	KEY

USB2: USB Front Panel Header

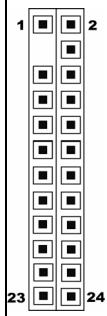


Signal	Pin	Pin	Signal
PWR_5V	1	2	PWR-5V
USB_N1	3	4	USB_N2
USB_P1	5	6	USB_P2
GND	7	8	GND
KEY	9	10	N/C

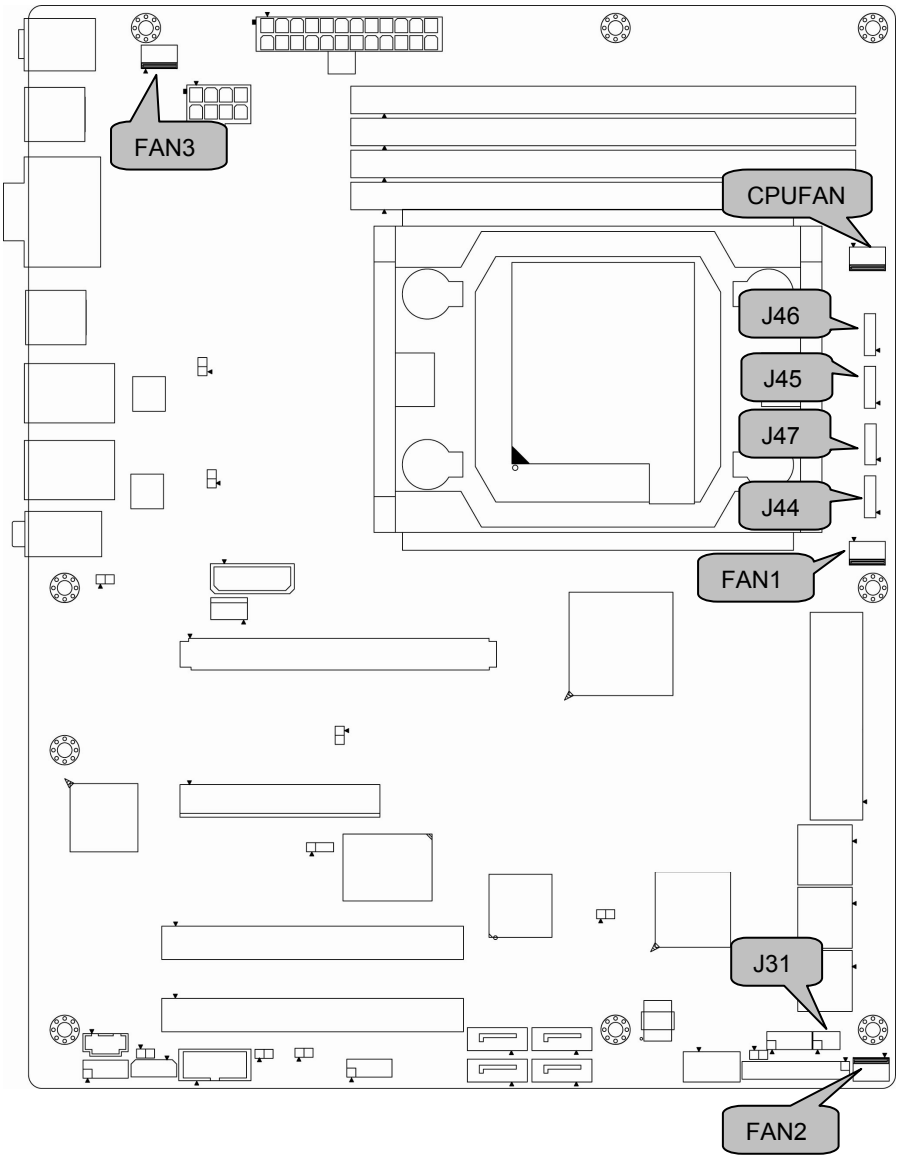
USB4: Type-A Vertical USB Connector

Pin	1	2	3	4
Signal	+5V	USB D-	USB D+	GND

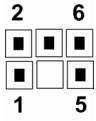
J30: TYFP-SSI Front Panel Header



Signal	Pin	Pin	Signal
PWR LED+	1	2	+3.3V AUX
KEY	3	4	ID LED+
PWR LED-	5	6	ID LED-
HDD LED+	7	8	FAULT LED1-
HDD LED-	9	10	FAULT LED2-
PWR SW+	11	12	LAN1 ACT LED+
PWR SW-	13	14	LAN2 ACT LED-
RST SW+	15	16	SMBUS DATA
RST SW-	17	18	SMBUS CLOCK
ID SW	19	20	INTRUSION#
NC	21	22	LAN2 ACT LED+
NMI#	23	24	LAN1 ACT LED-

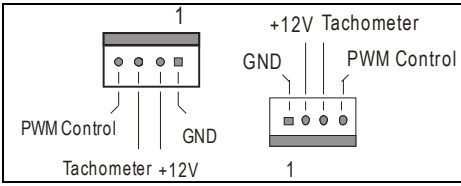

FAN3
CPUFAN
J46
J45
J47
J44
FAN1
J31
FAN2

J31: LCM Module Header (Barebones use only)



Signal	Pin	Pin	Signal
VCC	1	2	RXD2
KEY	3	4	GND
5VSB	5	6	TXD2
Use this header to connect the LCM module with system monitoring function. This header is reserved for barebones use.			

CPUFAN / FAN1 / FAN2 / FAN3: 4-Pin FAN Connector

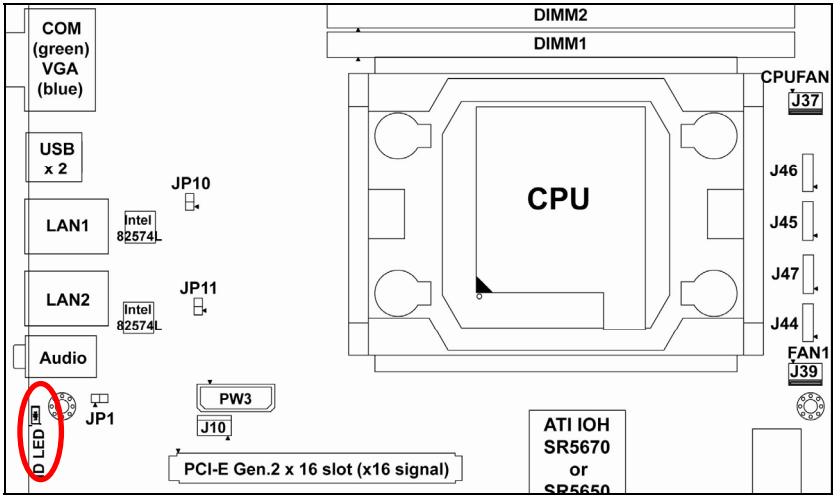


Use these headers to connect the cooling fan to your motherboard to keep the system at optimum performance levels.

J44 / J45 / J46 / J47: 8-Pin FAN Connector (Barebones use only)

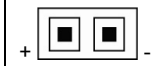
Pin	Signal
1	PWM1
2	VCC1
3	TACH1
4	GND
5	GND
6	TACH2
7	VCC2
8	PWM2

Use these headers to connect the cooling fan to your motherboard to keep the system at optimum performance levels. These headers are reserved for barebones use.



Onboard ID LED

You can identify the specific system using this LED. Users from remote site could also activate ID LED by input a few commands in IPMI, detailed software support please visit http://www.tyan.com for latest AST2050 user guide.

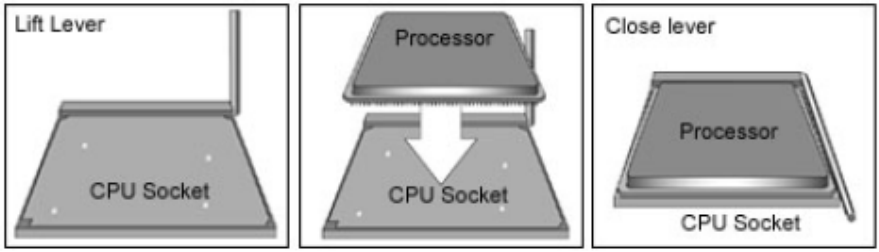


Pin	Signal	
+	P3V3_AUX	
-	SYS_ID_SW_N	
State	Color	Description
On	Blue	System identified
Off	Off	System not identified

2.4 Installing the Processor

The S8005 supported AMD® processors are listed in section ***Hardware Specifications*** on page 3. Check our website at http://www.tyan.com for latest processor support.

Note: MiTAC is not liable for damage as a result of operating an unsupported configuration.



The diagram is provided as a visual guide to help you to install the socket processor and may not be an exact representation of the processor you have.

Step 1: Lift the lever on the socket until it is approximately 90 or as far back as possible to the socket.
Step 2: Align the processor with the socket. There are keyed pins underneath the processor to ensure that the processor's installed correctly.
Step 3: Seat the processor firmly into the socket by gently pressing down until the processor sits flush with the socket.
Step 4: Place the socket lever back down until it locks into place. The installation is finished.

Take care when installing the processor as it has very fragile connector pins below the processor and can bend and break if inserted improperly.

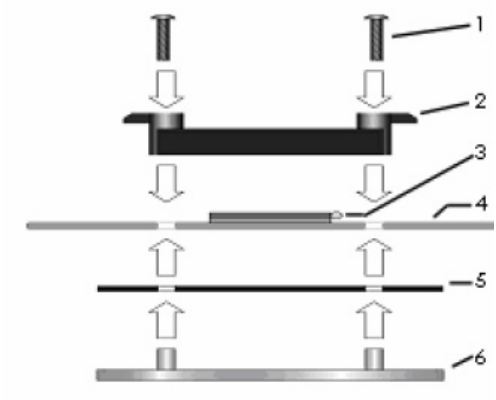
2.5 Heat sink Installation

After installing the processor, you should proceed to install the retention frame and heat sink. The CPU heat sink will ensure that the processor do not overheat and continue to operate at maximum performance for as long as you own them. The overheated processor is dangerous to the motherboard.

The backplate assembly prevents excessive motherboard flexing in the area near the processor and provides a base for the installation of the heatsink retention bracket and heatsink.

Because there are many different types of heatsinks available from many different manufacturers, a lot of them have their own methods of installation. For the safest method of installation and information on choosing the appropriate heat sink, using heat sinks validated by AMD®.
Please refer to AMD'®s website at www.amd.com.

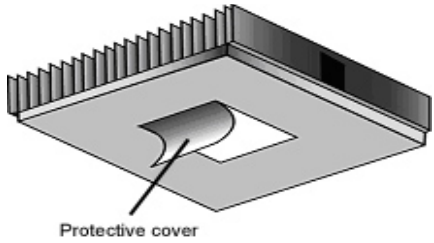
The following diagram illustrates how to install heat sink onto the CPU of S8005.



1. **Mounting screws**
2. **Heatsink retention frame**
3. **CPU socket**
4. **Motherboard PCB**
5. **Adhesive insulator material**
6. **Backplate assembly**

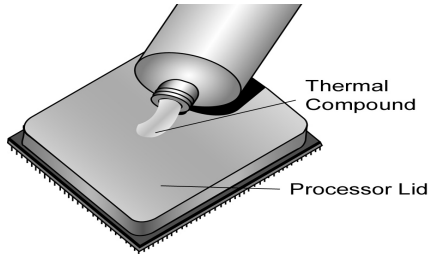
NOTE:
Please see next section for specific instructions on how to install mounting bracket.

2.6 Thermal Interface Material



There are two types of thermal interface materials designed for use with the processors.

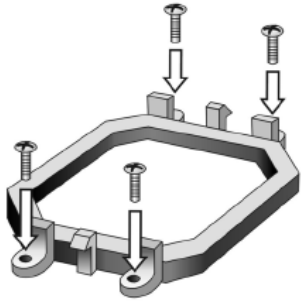
The most common material comes as a small pad attached to the heat sink at the time of purchase. There should be a protective cover over the material. Take care not to touch this material. Simply remove the protective cover and place the heat sink on the processor.



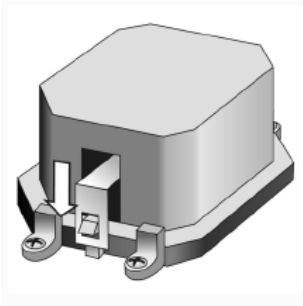
The second type of interface material is usually packaged separately. It is commonly referred to as 'thermal compound'. Simply apply a thin layer on to the CPU lid (applying too much will actually reduce the cooling).

NOTE: Always check with the manufacturer of the heat sink & processor to ensure that the thermal interface material is compatible with the processor and meets the manufacturer's warranty requirements.

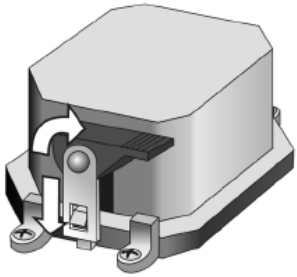
2.7 Heatsink Installation Procedures



1. After placing backplate and interface material under motherboard place heatsink retention frame on top of motherboard. Align plastic retention bracket screw holes with CPU backplate standoffs.
Tighten screws to secure plastic retention bracket. Repeat for the other side. **DO NOT OVER TIGHTEN.**



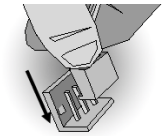
2. After tightening screws secure metal clip to plastic retention bracket center tab. Repeat for the other side of heatsink.



3. After securing metal clip to plastic retention bracket center tab, push down on plastic clip to lock plastic clip to side tab.

2.8 Finishing Installing the Heat sink

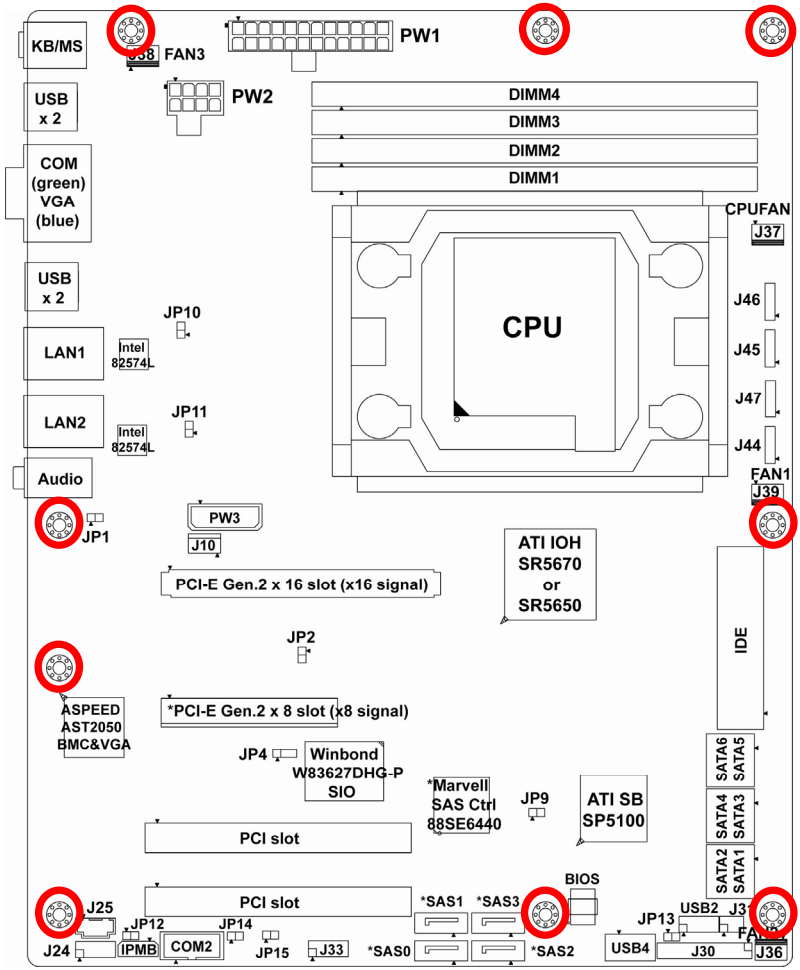
After you have finished installing the heat sink onto the processor and socket, attach the end wire of the fan (which should already be attached to the heat sink) to the motherboard. The following diagram illustrates how to connect fans onto the motherboard.



Once you have finished installing all the fans you can connect your drives (hard drives, CD-ROM drives, etc.) to your motherboard.

2.9 Tips on Installing Motherboard in Chassis

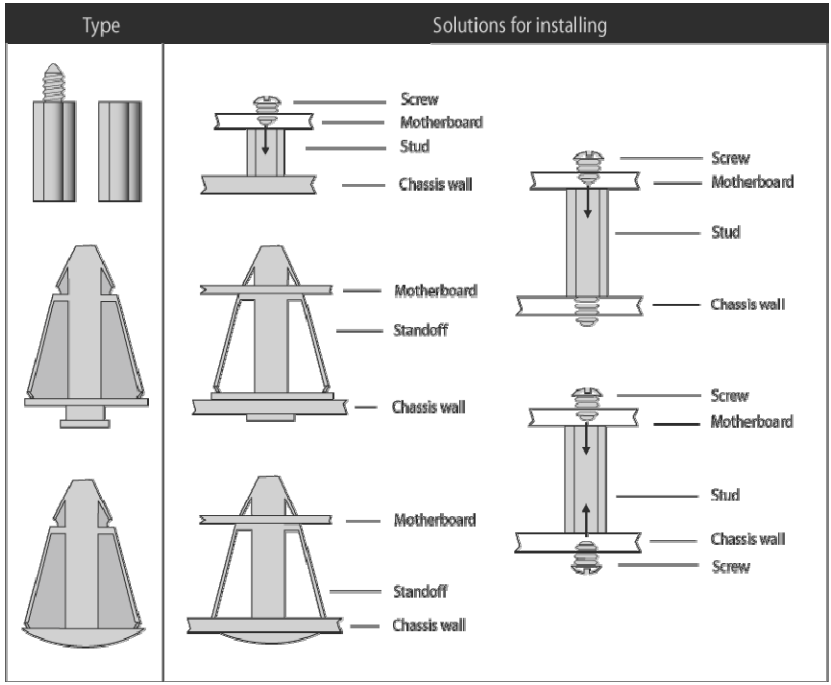
Before installing your motherboard, make sure your chassis has the necessary motherboard support studs installed. These studs are usually metal and are gold in color. Usually, the chassis manufacturer will pre-install the support studs. If you are unsure of stud placement, simply lay the motherboard inside the chassis and align the screw holes of the motherboard to the studs inside the case. If there are any studs missing, you will know right away since the motherboard will not be able to be securely installed.



Some chassis' include plastic studs instead of metal. Although the plastic studs are usable, MiTAC recommends using metal studs with screws that will fasten the motherboard more securely in place.

Below is a chart detailing what the most common motherboard studs look like and how they should be installed.

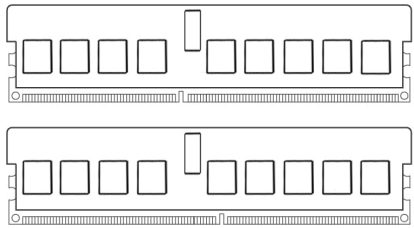
Mounting the Motherboard



2.10 Installing the Memory

Before installing memory, ensure that the memory you have is compatible with the motherboard and processor. Check the TYAN® web site at www.tyan.com for details of the type of memory recommended for your motherboard.

The following diagram shows common memory modules:



- All installed memory will automatically be detected and no jumpers or settings will be necessary.
- Supports up to 16GB of Unbuffered ECC DDR3 667/800/1066/1333* MHz memory.

Note: If you use 2 UNB/ECC DDR3 1333MHz modules on the same channel, then the max speed will be 1066MHz.

Memory Population Rule

(Note: X indicates a populated DIMM Slot)

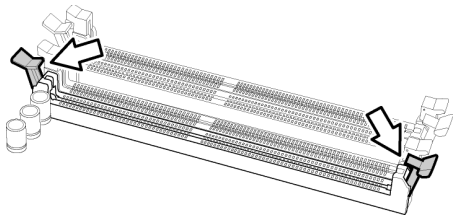
DIMM Slot	DIMM1	DIMM2	DIMM3	DIMM4
64 bits Support	X			
			X	
	X		X	
128 bits support	X	X		
			X	X
	X	X	X	X

NOTE: For the DIMM number, please refer to the motherboard layout in section ***2.3 Board Parts, Jumpers and Connectors*** on page 10 for memory installation.

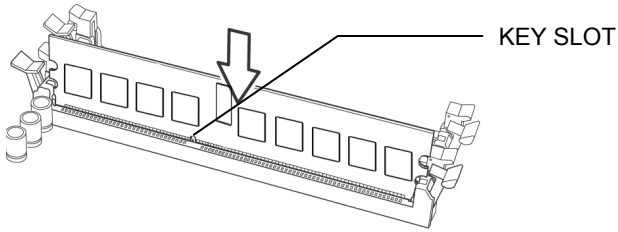
Memory Installation Procedure

Follow these instructions to install memory modules into the S8005.

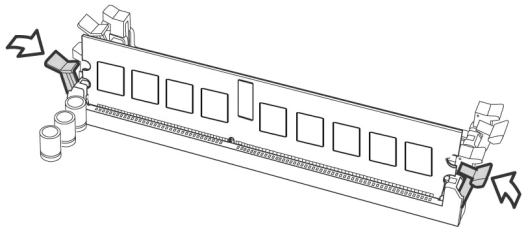
1. Press the locking levers in the direction shown in the following illustration.



2. Align the memory module with the socket. The memory module is keyed to fit only one way in the socket.



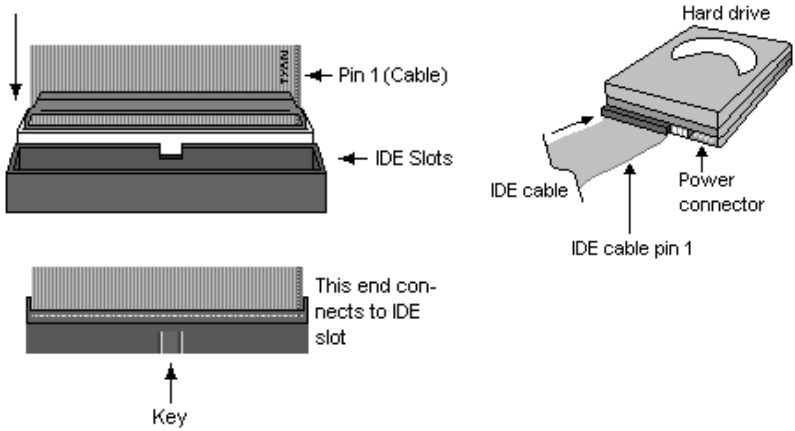
3. Seat the module firmly into the socket by gently pressing down until it sits flush with the socket. The locking levers pop up into place.



2.11 Attaching Drive Cables

Attaching the IDE drive cable is simple. These cables are “keyed” to only allow them to be connected in the correct manner. TYAN motherboards have two on-board IDE channels, each supporting two drives. The black connector designates the Primary channel, while the white connector designates the Secondary channel.

Attaching IDE cables to the IDE connectors is illustrated below:



Simply plug in the BLUE END of the IDE cable into the motherboard IDE connector, and the other end(s) into the drive(s). Each standard IDE cable has three connectors, two of which are closer together. The BLUE connector that is furthest away from the other two is the end that connects to the motherboard. The other two connectors are used to connect to drives.

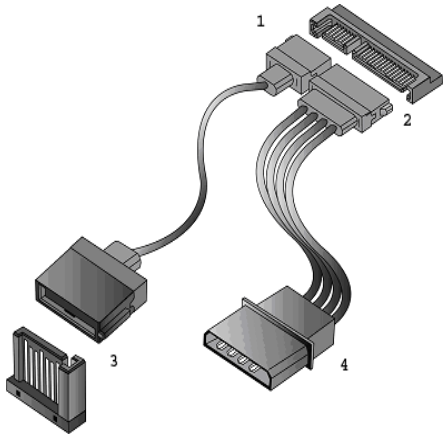
Note: Always remember to properly set the drive jumpers. If only using one device on a channel, it must be set as Master for the BIOS to detect it.

TIP: Pin 1 on the IDE cable (usually designated by a colored wire) faces the drive’s power connector.

Attaching Serial ATA Cables

S8005 is equipped with 6 Serial ATA (SATA) channels and 4 SAS connectors.
Connections for the drives are very simple.
There is no need to set Master/Slave jumpers on SATA drives.
If you are in need of other SATA/SAS cables or power adapters please contact your place of purchase.

The following pictures illustrate how to connect an SATA drive.



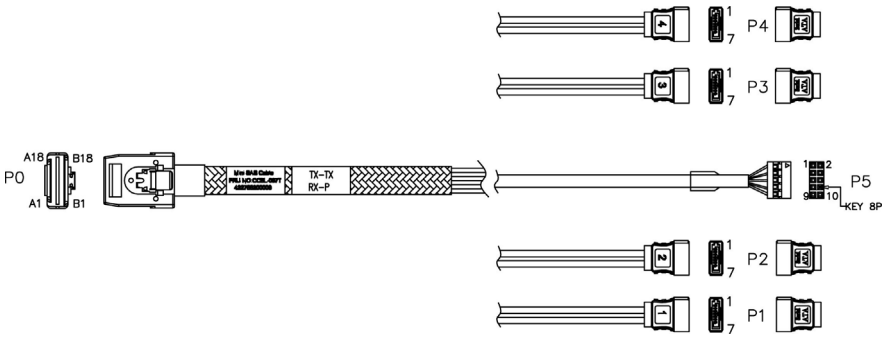
1. SATA drive cable connection

2. SATA drive power connection

3. SATA cable motherboard connector

4. SATA drive power adapter

Attaching SAS Cables



P0 Mini-SAS backplane connector

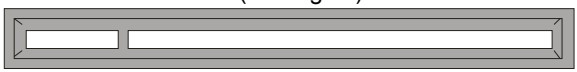
P1, P2, P3, P4 SAS cable motherboard connector

P5 SAS SGPIO connector (reserved)

2.12 Installing Add-In Cards

Before installing add-in cards, it's helpful to know if they are fully compatible with your motherboard. For this reason, we've provided the diagrams below, showing the slots that may appear on your motherboard.

PCI-E Gen. 2 x16 slot (x16 signal)



PCI-E Gen.2 x8 slot (x8 signal)

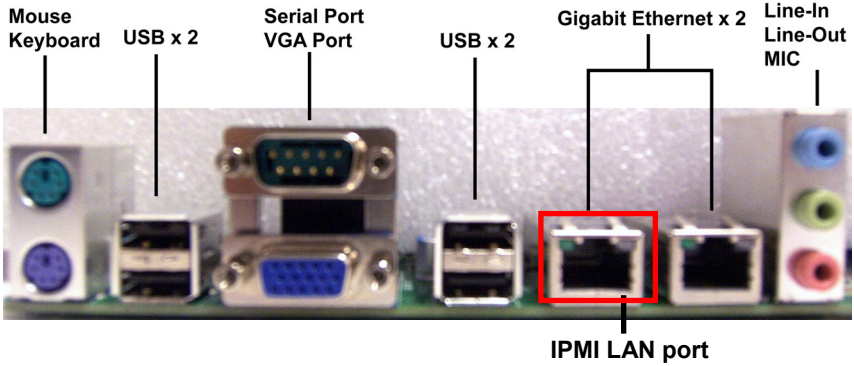
Simply find the appropriate slot for your add-in card and insert the card firmly. Do not force any add-in cards into any slots if they do not seat in place. It is better to try another slot or return the faulty card rather than damaging both the motherboard and the add-in card.

TIP: It's a good practice to install add-in cards in a staggered manner rather than making them directly adjacent to each other. Doing so allows air to circulate within the chassis more easily, thus improving cooling for all installed devices.

NOTE: You must always unplug the power connector to the motherboard before performing system hardware changes to avoid damaging the board or expansion device.

2.13 Connecting External Devices

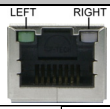
Your motherboard supports a number of different interfaces through connecting peripherals. See the following diagrams for the details.



NOTE: Peripheral devices can be plugged straight into any of these ports but software may be required to complete the installation.

Onboard LAN LED Color Definition

The two onboard Ethernet ports have green and yellow LEDs to indicate LAN status. The chart below illustrates the different LED states.

10/100/1000 Mbps LAN Link/Activity LED Scheme			
		**Left LED**	**Right LED**
10 Mbps	**Link**	Green	Off
	Active	Blinking Green	Off
100 Mbps	**Link**	Green	Green
	Active	Blinking Green	Green
1000 Mbps	**Link**	Green	Orange
	Active	Blinking Green	Orange
No Link		Off	Off

2.14 Installing the Power Supply

There are two power connectors on your S8005. The S8005 requires 2 power inputs:

- 24-pin (PW1)

Signal	Pin	Pin	Signal
+3.3V	1	13	+3.3V
+3.3V	2	14	-12V
GND	3	15	GND
+5V	4	16	PS_ON
GND	5	17	GND
+5V	6	18	GND
GND	7	19	GND
PS_GD	8	20	Reset
5VSB	9	21	+5V
+12V	10	22	+5V
+12V	11	23	+5V
+3.3V	12	24	GND

- 8-pin (PW2)

Signal	Pin	Pin	Signal
GND	1	5	+12V
GND	2	6	+12V
GND	3	7	+12V
GND	4	8	+12V

NOTE: Please be aware that ATX 2.x, ATX12V and ATXGES power supplies may **not** be compatible with the board and can damage the motherboard and/or CPU(s).

Applying power to the board:
Connect the 12V 8-pin power connector.
Connect the EPS/12V 24-pin power connector.
Connect power cable to power supply and power outlet.

NOTE: You must unplug the power supply before plugging the power cables to motherboard connectors.

2.15 Finishing Up

Congratulations on making it this far! You have finished setting up the hardware aspect of your computer. Before closing up your chassis, make sure that all cables and wires are connected properly, especially IDE cables and most importantly, jumpers. You may have difficulty powering on your system if the motherboard jumpers are not set correctly. In the rare circumstance that you have experienced difficulty, you can find help by asking your vendor for assistance. If they are not available for assistance, please find setup information and documentation online at our website or by calling your vendor's support line.

Chapter 3: BIOS Setup

3.1 About the BIOS

The BIOS is the basic input/output system, the firmware on the motherboard that enables your hardware to interface with your software. The BIOS determines what a computer can do without accessing programs from a disk. The BIOS contains all the code required to control the keyboard, display screen, disk drives, serial communications, and a number of miscellaneous functions. This chapter describes the various BIOS settings that can be used to configure your system.

The BIOS section of this manual is subject to change without notice and is provided for reference purposes only. The settings and configurations of the BIOS are current at the time of print and are subject to change, and therefore may not match exactly what is displayed on screen.

This section describes the BIOS setup program. The setup program lets you modify basic configuration settings. The settings are then stored in a dedicated, battery-backed memory (called NVRAM) that retains the information even when the power is turned off.

To start the BIOS setup utility:

1. Turn on or reboot your system.
2. Press <Del> during POST (F4 on remote console) to start the BIOS setup utility.

3.1.1 Setup Basics

The table below shows how to navigate in the setup program using the keyboard.

Key	Function
Tab	Moves from one selection to the next
Left/Right Arrow Keys	Changes from one menu to the next
Up/Down Arrow Keys	Moves between selections
Enter	Opens highlighted section
PgUp/PgDn Keys	Changes settings.

3.1.2 Getting Help

Pressing [**F1**] will display a small help window that describes the appropriate keys to use and the possible selections for the highlighted item. To exit the Help Window, press [**ESC**] or the [**F1**] key again.

3.1.3 In Case of Problems

If you have trouble booting your computer after making and saving the changes with the BIOS setup program, you can restart the computer by holding the power button down until the computer shuts off (usually within 4 seconds); resetting by pressing CTRL-ALT-DEL; or clearing the CMOS.
The best advice is to only alter settings that you thoroughly understand. In particular, do not change settings in the Chipset section unless you are absolutely sure of what you are doing. The Chipset defaults have been carefully chosen either by MiTAC or your system manufacturer for best performance and reliability. Even a seemingly small change to the Chipset setup options may cause the system to become unstable or unusable.

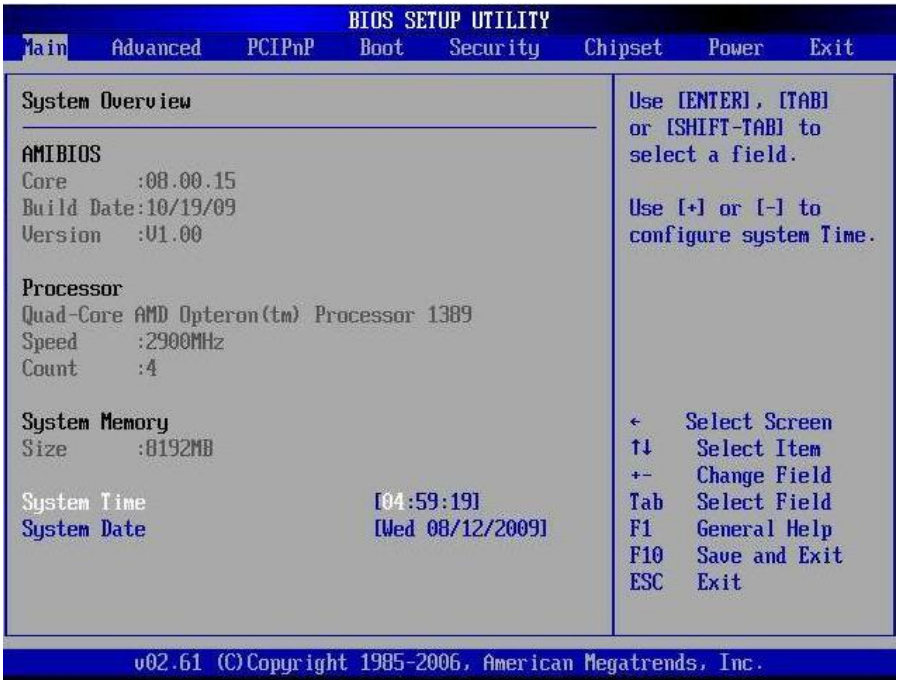
3.1.4 Setup Variations

Not all systems have the same BIOS setup layout or options. While the basic look and function of the BIOS setup remains more or less the same for most systems, the appearance of your Setup screen may differ from the charts shown in this section. Each system design and chipset combination requires a custom configuration. In addition, the final appearance of the Setup program depends on the system designer. Your system designer may decide that certain items should not be available for user configuration, and remove them from the BIOS setup program.

NOTE: The following pages provide the details of BIOS menu. Please be noticed that the BIOS menu are continually changing due to the BIOS updating. The BIOS menu provided are the most updated ones when this manual is written. Please visit TYAN's website at http://www.tyan.com for the information of BIOS updating.

3.2 BIOS Main Menu

In this section, you can alter general features such as the date and time.
Note that the options listed below are for options that can directly be changed within the Main Setup screen.



AMIBIOS
This shows the core version, build date and version of BIOS,

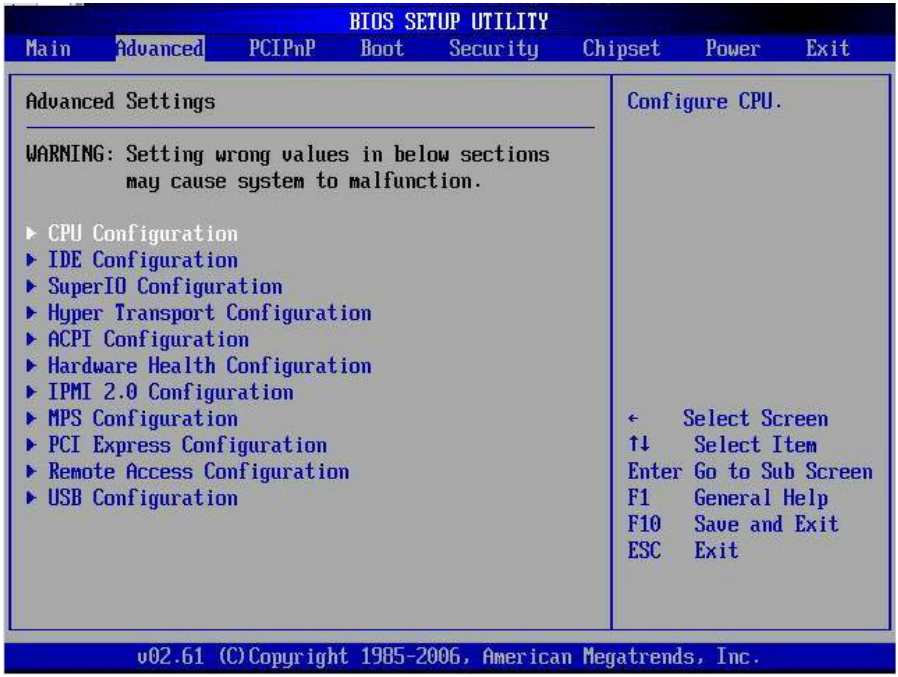
Processor
This displays CPU information.

System Memory
This displays the amount of system memory present on the system.

System Time / System Date
System Time: Adjusts the system clock.
HHHours (24hr. format): MMMinutes : SSSeconds
System Date: Adjusts the system date.
MMMonths : DDDays : YYYYYears

3.3 BIOS Advanced Menu

This section facilitates configuring advanced BIOS options for your system.



CPU Configuration

Configure CPU.

IDE Configuration

Configure the IDE devices.

SuperIO Configuration

Configure Super IO.

Hyper Transport Configuration

Configure HT links.

ACPI Configuration

Section for Advanced ACPI Configuration.

Hardware Health Configuration

Configure/monitor the Hardware Health.

IPMI 2.0 Configuration

IPMI configuration including server monitoring and event log.

MPS Configuration

Configure the Multi-Processor Table.

PCI Express Configuration

Configure PCI Express Support.

Remote Access Configuration

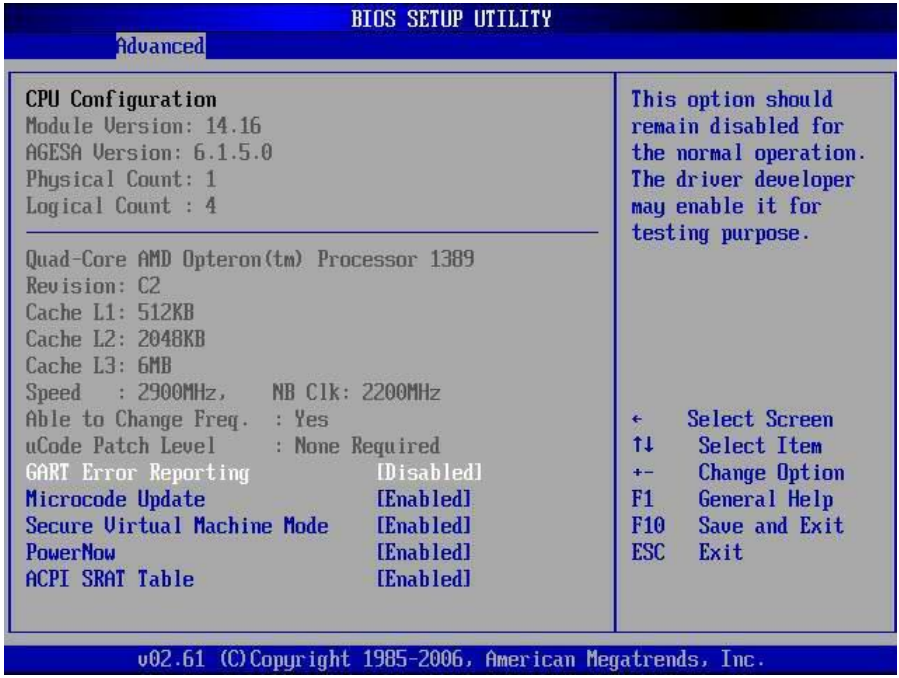
Configure Remote Access.

USB Configuration

Configure the USB support.

3.3.1 Advanced CPU Configuration

This section allows you to fine-tune the processor options.


BIOS SETUP UTILITY
Advanced
CPU Configuration
Module Version: 14.16
AGESA Version: 6.1.5.0
Physical Count: 1
Logical Count : 4
Quad-Core AMD Opteron(tm) Processor 1389
Revision: C2
Cache L1: 512KB
Cache L2: 2048KB
Cache L3: 6MB
Speed : 2900MHz, NB Clk: 2200MHz
Able to Change Freq. : Yes
uCode Patch Level : None Required
GART Error Reporting [Disabled]
Microcode Update [Enabled]
Secure Virtual Machine Mode [Enabled]
PowerNow [Enabled]
ACPI SRAT Table [Enabled]
This option should remain disabled for the normal operation. The driver developer may enable it for testing purpose.
← Select Screen
↑↓ Select Item
+- Change Option
F1 General Help
F10 Save and Exit
ESC Exit
v02.61 (C)Copyright 1985-2006, American Megatrends, Inc.

GART Error Reporting
This option should remain disabled for the normal operation. The driver developer may enable it for testing purpose
Disabled / Enabled

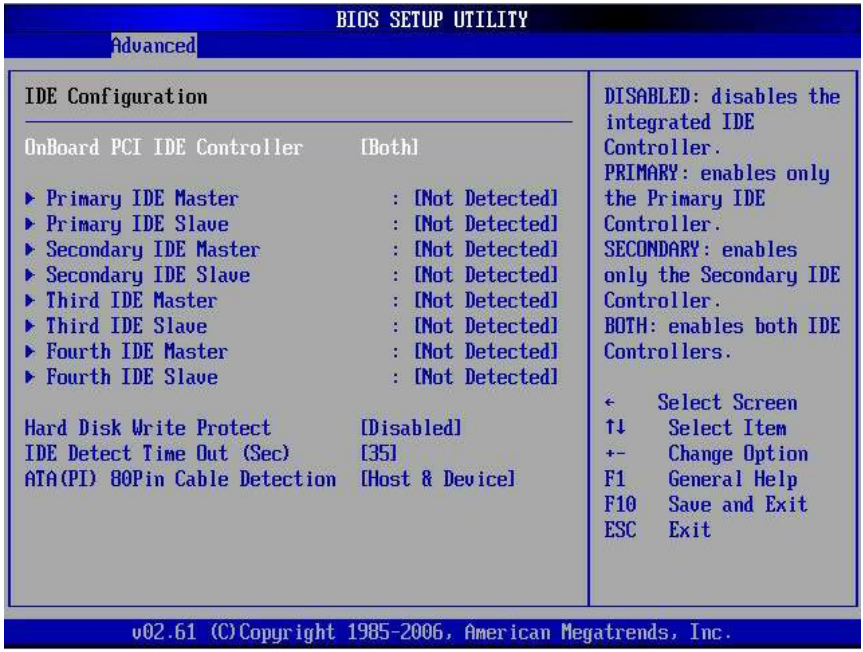
Microcode Update
Disabled / **Enabled**

Secure Virtual Machine Mode
Disabled / **Enabled**

PowerNow
Enable/disable the generation of ACPI_PPC, _PSS, and _PCT objects.
Disabled / **Enabled**

ACPI SRAT Table
Enable or disable the building of ACPI SRAT Table.
Disabled / **Enabled**

3.3.2 Advanced IDE Configuration


BIOS SETUP UTILITY
Advanced
IDE Configuration
OnBoard PCI IDE Controller [Both]
▶ Primary IDE Master : [Not Detected]
▶ Primary IDE Slave : [Not Detected]
▶ Secondary IDE Master : [Not Detected]
▶ Secondary IDE Slave : [Not Detected]
▶ Third IDE Master : [Not Detected]
▶ Third IDE Slave : [Not Detected]
▶ Fourth IDE Master : [Not Detected]
▶ Fourth IDE Slave : [Not Detected]
Hard Disk Write Protect [Disabled]
IDE Detect Time Out (Sec) [35]
ATA(PI) 80Pin Cable Detection [Host & Device]
DISABLED: disables the integrated IDE Controller.
PRIMARY: enables only the Primary IDE Controller.
SECONDARY: enables only the Secondary IDE Controller.
BOTH: enables both IDE Controllers.
← Select Screen
↑↓ Select Item
+- Change Option
F1 General Help
F10 Save and Exit
ESC Exit
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OnBoard PCI IDE Controller
DISABLED: disables the integrated IDE Controller.
PRIMARY: enables only the Primary IDE Controller.
SECONDARY: enables only the Secondary IDE Controller.
BOTH: enables both IDE Controllers.

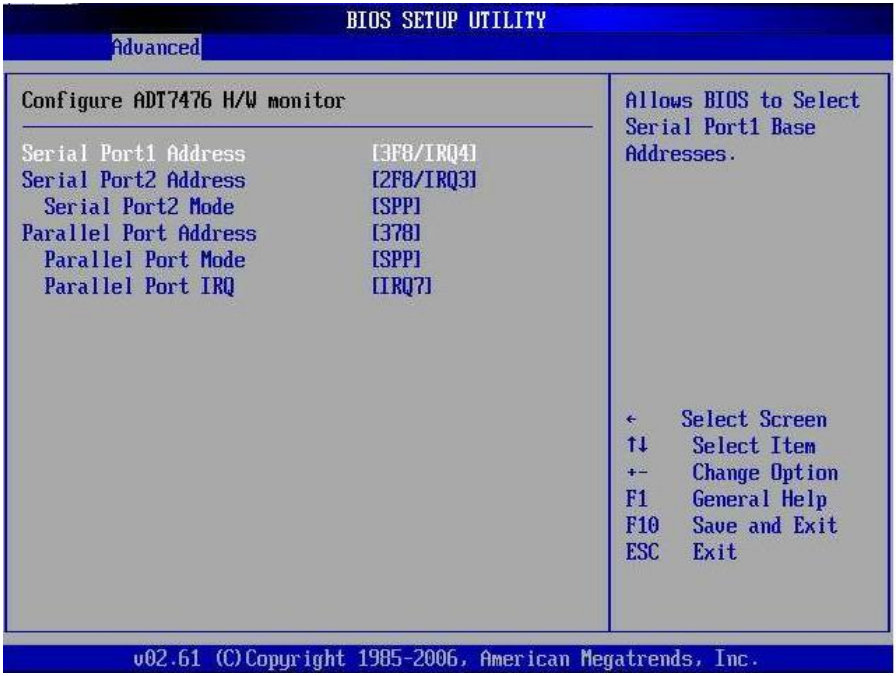
Primary/Secondary/Third/Fourth IDE Master
While entering setup, BIOS auto detects the presence of IDE devices. This displays the status of auto detection of IDE devices.

Hard Disk Write Protect
Disable/Enable device write protection. This will be effective only if device is accessed through BIOS.
Disabled / Enabled

IDE Detect Time Out (Sec)
Select the time out value for detecting ATA/ATAPI device(s).
0~35 (at 5 interval)

ATA(PI) 80Pin Detection
Select the mechanism for detecting 80Pin ATA(PI) Cable. [Host & Device]

3.3.3 Advanced SuperIO Configuration



Serial Port1 Address
Allows BIOS to Select Serial Port 1Base Address.
Disabled / **[3F8/IRQ4]** / [2E8/IRQ3]

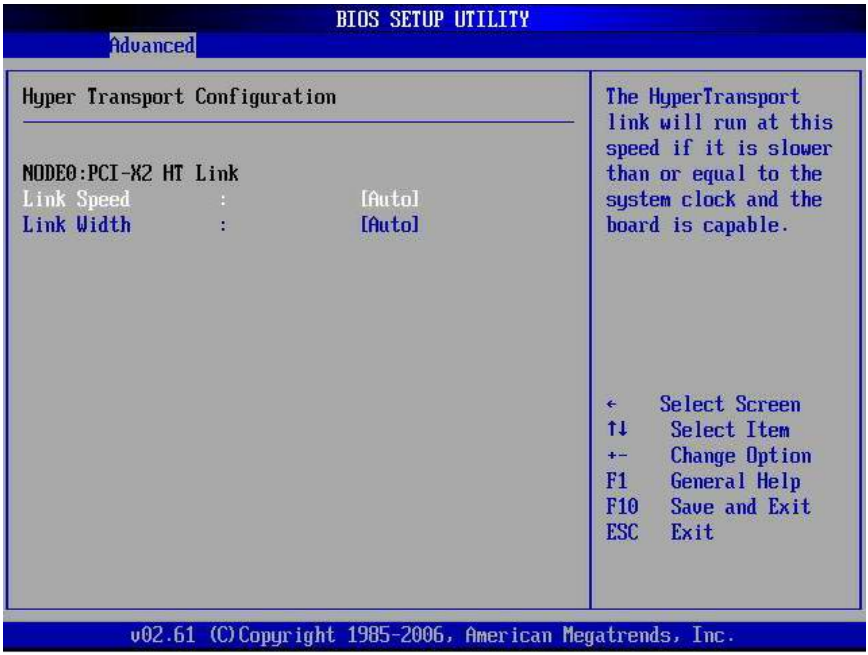
Serial Port2 Address
Allows BIOS to Select Serial Port 2 Base Address.
Disabled / **[2F8/IRQ3]** / [2E8/IRQ3]

Serial Port2 Mode
Allows BIOS to Select mode for Serial Port 2.
Normal / IrDA / Ask IR

Parallel Port Address
Allows BIOS to Select Parallel Port Base Address.
Disabled / 378 / 278 / 3BC

Parallel Port IRQ
Allows BIOS to Select Parallel Port IRQ.
IRQ5 / **IRQ7**

3.3.4 Advanced Hyper Transport Configuration


BIOS SETUP UTILITY
Advanced
Hyper Transport Configuration
NODE0:PCI-X2 HT Link
Link Speed : [Auto]
Link Width : [Auto]
The HyperTransport link will run at this speed if it is slower than or equal to the system clock and the board is capable.
← Select Screen
↑↓ Select Item
+- Change Option
F1 General Help
F10 Save and Exit
ESC Exit
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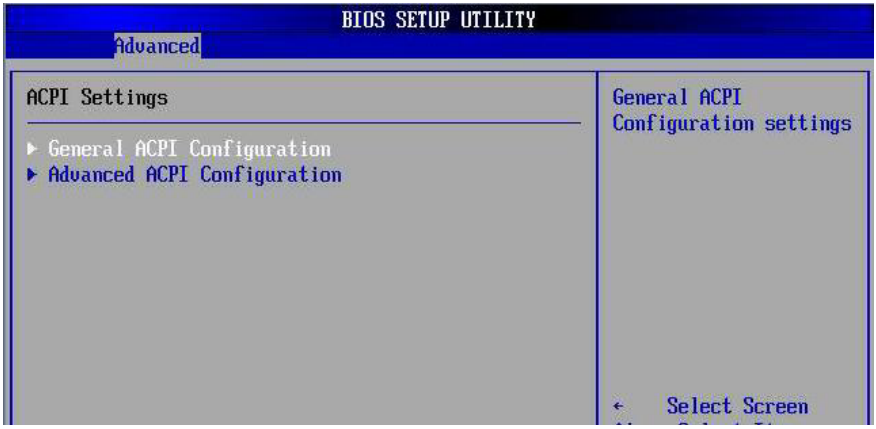
Link speed

The HyperTransport link will run at this speed if it is slower than or equal to the system clock and the board is capable.

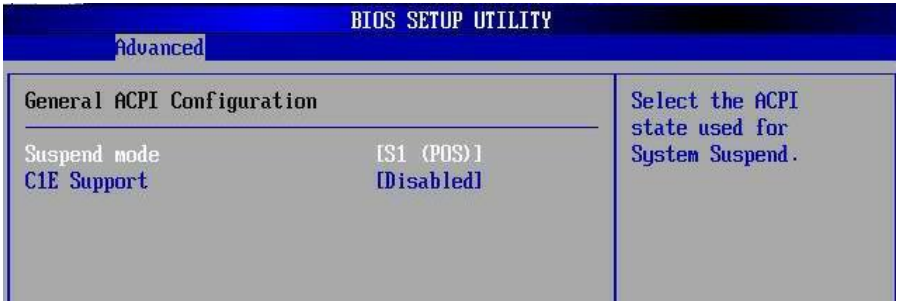
Link Width

The HyperTransport link will run at this width.

3.3.5 Advanced ACPI Configuration



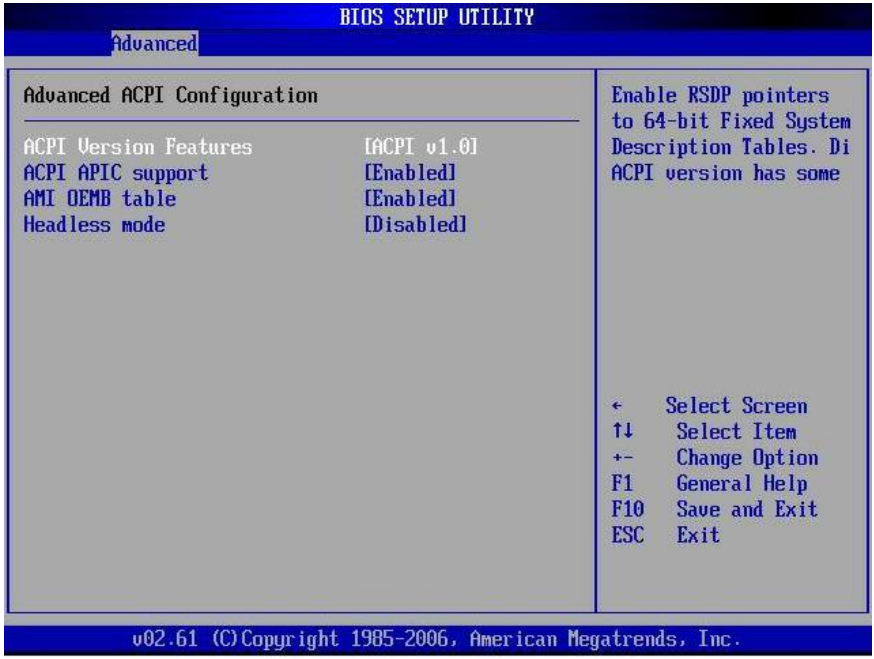
3.3.5.1 General ACPI Configuration



Suspend Mode
Select the ACPI state used for System Suspend.
Auto / S1(POS) / S3(STR)

C1E Support
Disabled / Enable

3.3.5.2 Advanced ACPI Configuration


BIOS SETUP UTILITY
Advanced
Advanced ACPI Configuration
ACPI Version Features [ACPI v1.0]
ACPI APIC support [Enabled]
AMI OEMB table [Enabled]
Headless mode [Disabled]
Enable RSDP pointers to 64-bit Fixed System Description Tables. Di ACPI version has some
← Select Screen
↑↓ Select Item
+- Change Option
F1 General Help
F10 Save and Exit
ESC Exit
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ACPI Version Features
Set this value to allow or prevent the system to be complaint with the ACPI 2.0 specification.
ACPI 3.0 / **ACPI 2.0** / ACPI 1.0

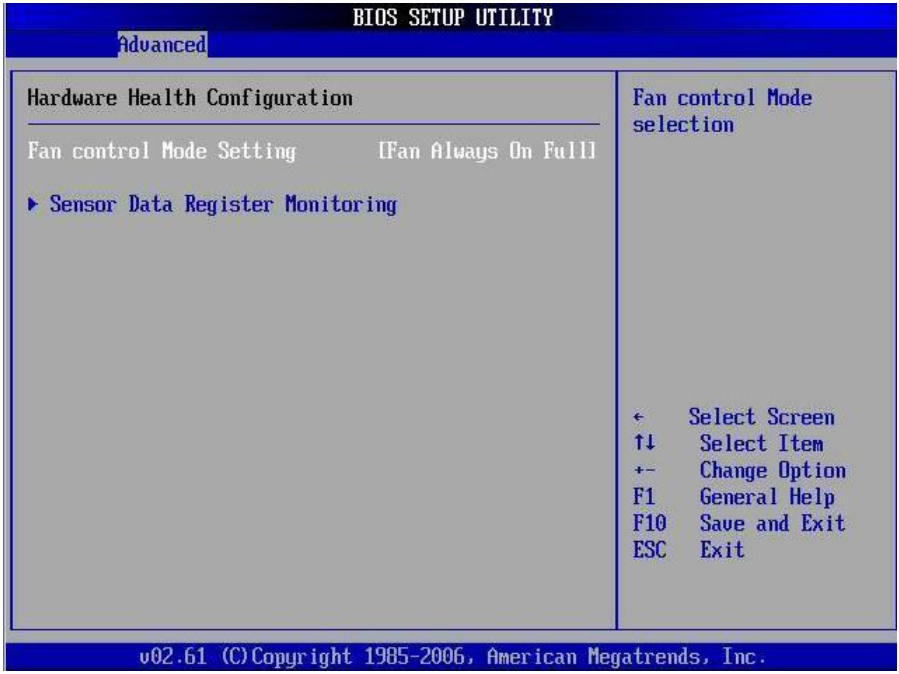
ACPI APIC Support
This option allows you to define whether or not to enable APIC features.
Enabled / Disabled

AMI OEMB Table
Set this value to allow the ACPI BIOS to add a pointer to an OEMB table in the Root System Description Table (RSDT) table.
Enabled / Disabled

> **NOTE:** OEMB table is used to pass POST data to the AMI code during ACPI O/S operations.

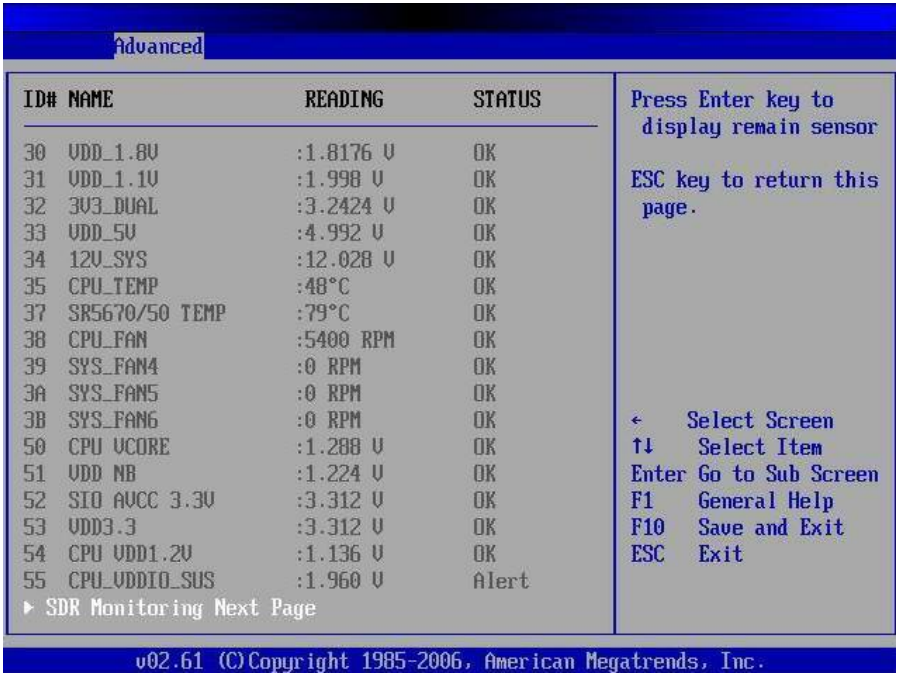
Headless Mode
Enable or disable Headless operation mode through ACPI.
Disabled / Enabled

3.3.6 Hardware Health Configuration



Fan control Mode Setting
Fan control Mode selection
Auto Fan Mode / **Fan Always On Full**

3.3.6.1 Sensor Data Register Monitoring


Advanced
ID# NAME READING STATUS
30 VDD_1.8V :1.8176 V OK
31 VDD_1.1V :1.998 V OK
32 3V3_DUAL :3.2424 V OK
33 VDD_5V :4.992 V OK
34 12V_SYS :12.028 V OK
35 CPU_TEMP :48°C OK
37 SR5670/50 TEMP :79°C OK
38 CPU_FAN :5400 RPM OK
39 SYS_FAN4 :0 RPM OK
3A SYS_FAN5 :0 RPM OK
3B SYS_FAN6 :0 RPM OK
50 CPU VCORE :1.288 V OK
51 VDD NB :1.224 V OK
52 SIO AVCC 3.3V :3.312 V OK
53 VDD3.3 :3.312 V OK
54 CPU VDD1.2V :1.136 V OK
55 CPU_VDDIO_SUS :1.960 V Alert
▶ SDR Monitoring Next Page
Press Enter key to display remain sensor
ESC key to return this page.
← Select Screen
↑↓ Select Item
Enter Go to Sub Screen
F1 General Help
F10 Save and Exit
ESC Exit
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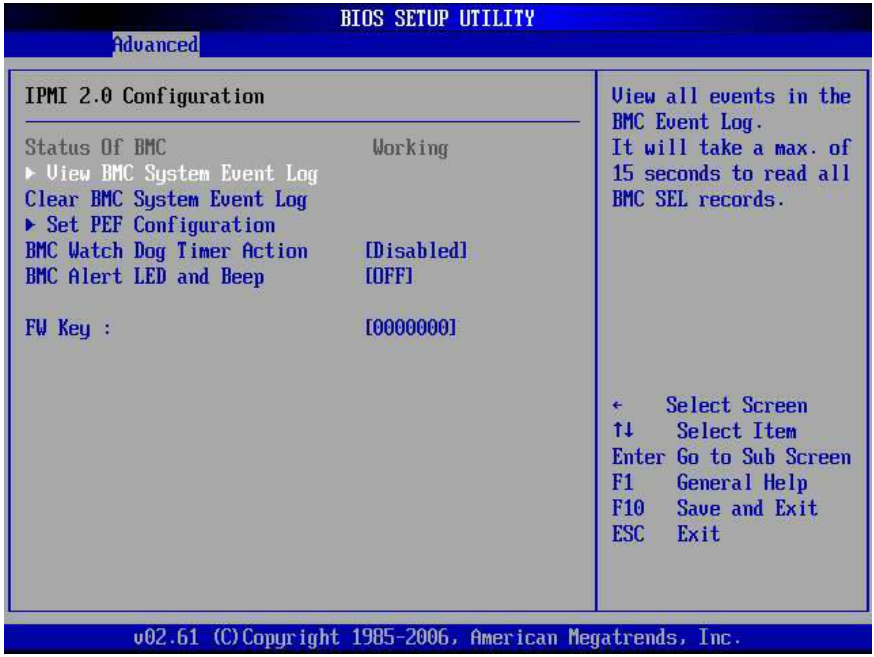
Read only. It can not be modified in user mode.

3.3.7 IPMI 2.0 Configuration


BIOS SETUP UTILITY
Advanced
IPMI 2.0 Configuration
Status Of BMC Working
▸ View BMC System Event Log
Clear BMC System Event Log
▸ Set PEF Configuration
BMC Watch Dog Timer Action [Disabled]
BMC Alert LED and Beep [OFF]
FW Key : [0000000]
View all events in the BMC Event Log. It will take a max. of 15 seconds to read all BMC SEL records.
← Select Screen
↑↓ Select Item
Enter Go to Sub Screen
F1 General Help
F10 Save and Exit
ESC Exit
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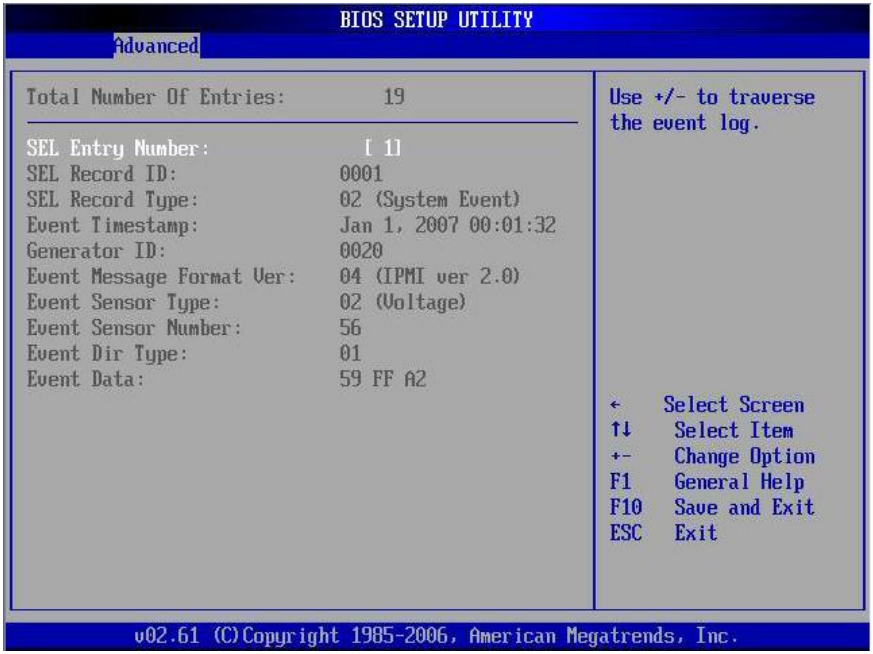
Clear BMC System Event Log
View all events in the BMC Event Log. It will take a max. of 15 seconds to read all BMC SEL records.

BMC Watch Dog Timer Action
Allows the BMC to reset or power down the system if the operating system crashes or hangs.
Disabled / Enable

BMC Alert LED and Beep
BMC Alert LED and Beep
ON / **OFF**

FW Key
Enter IPMI FW Key upgrade to IPMI or iKVM function.
[0000000]

3.3.7.1 View BMC System Event Log


BIOS SETUP UTILITY
Advanced
Total Number Of Entries: 19
SEL Entry Number: [1]
SEL Record ID: 0001
SEL Record Type: 02 (System Event)
Event Timestamp: Jan 1, 2007 00:01:32
Generator ID: 0020
Event Message Format Ver: 04 (IPMI ver 2.0)
Event Sensor Type: 02 (Voltage)
Event Sensor Number: 56
Event Dir Type: 01
Event Data: 59 FF A2
Use +/- to traverse the event log.
← Select Screen
↑↓ Select Item
+- Change Option
F1 General Help
F10 Save and Exit
ESC Exit
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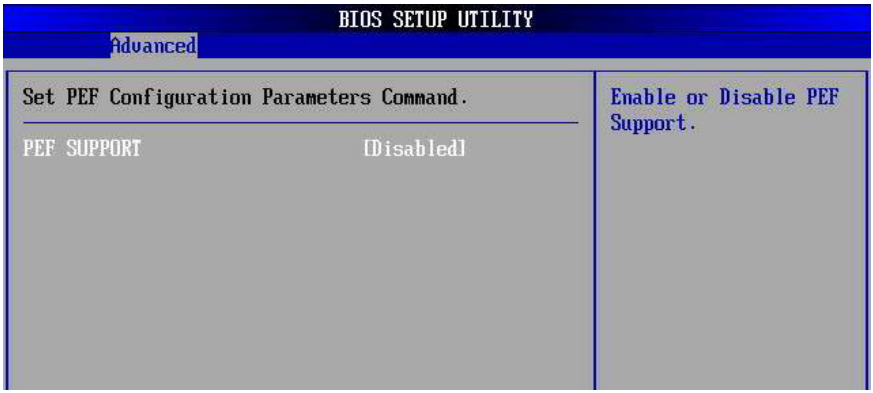
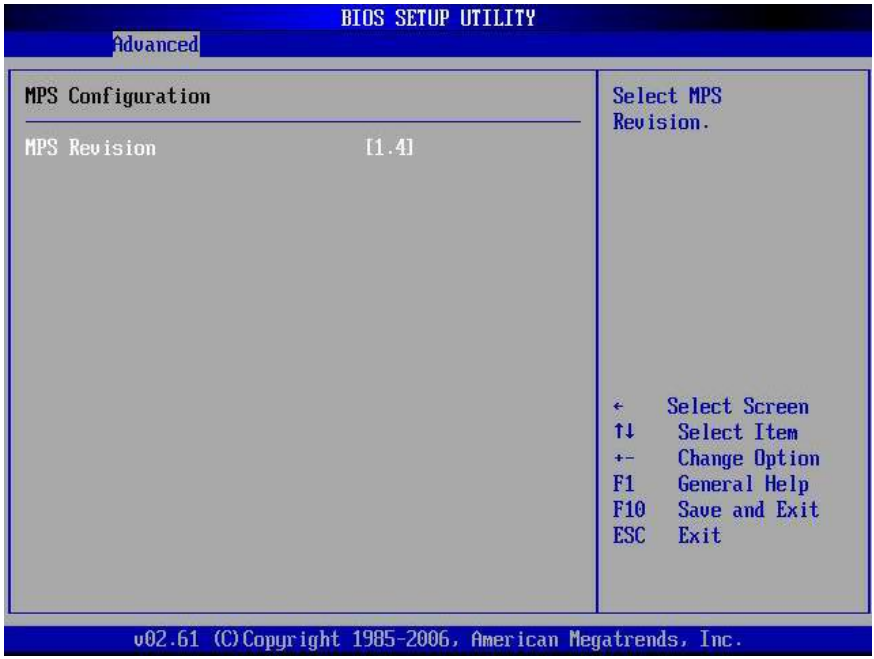
3.3.7.2 Set PEF Configuration



Enable or Disable PEF support.

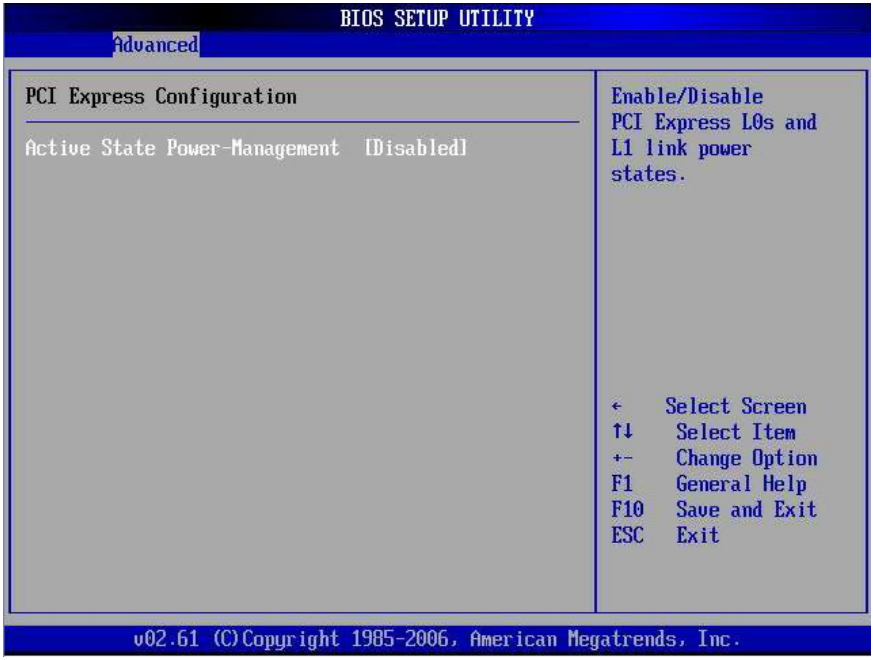
3.3.8 MPS Configuration

Configure the Multi-Processor Table.



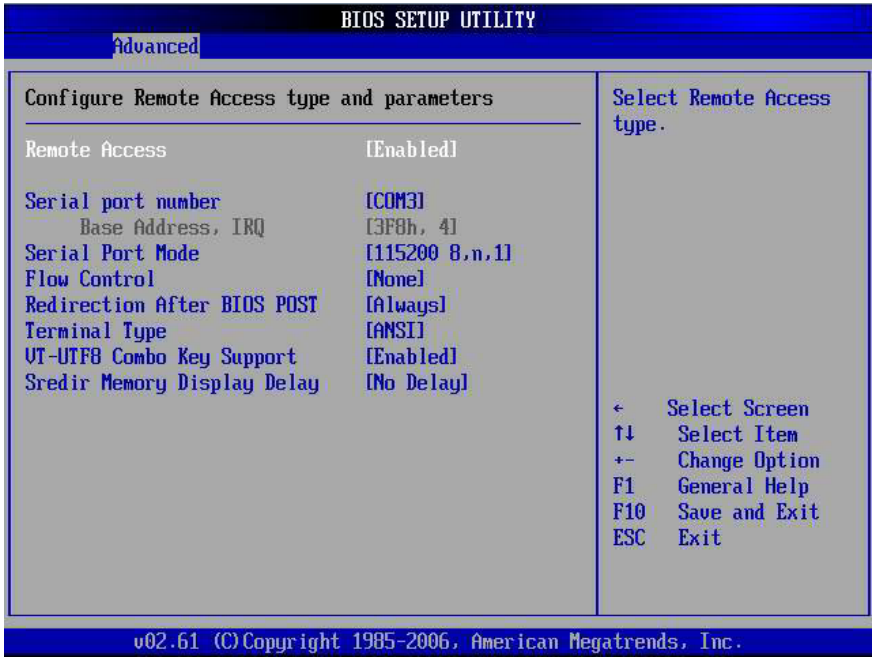
Select MPS Revision
[1.1] / **[1.4]**

3.3.9 PCI-Express Configuration



Enable or Disable PCI Express L0 and L1 link power states.

3.3.10 Remote Access Configuration



Remote Access
Enables remote access to system through serial port.
Disabled / Enabled

Serial port number
Select Serial Port for console redirection. Make sure the selected port is enabled.
COM1/ COM2 / COM3 (virtual for BMC)

Serial Port Mode
Select Serial Port settings.

Flow Control
Select Flow Control for console redirection.
None / Hardware / Software

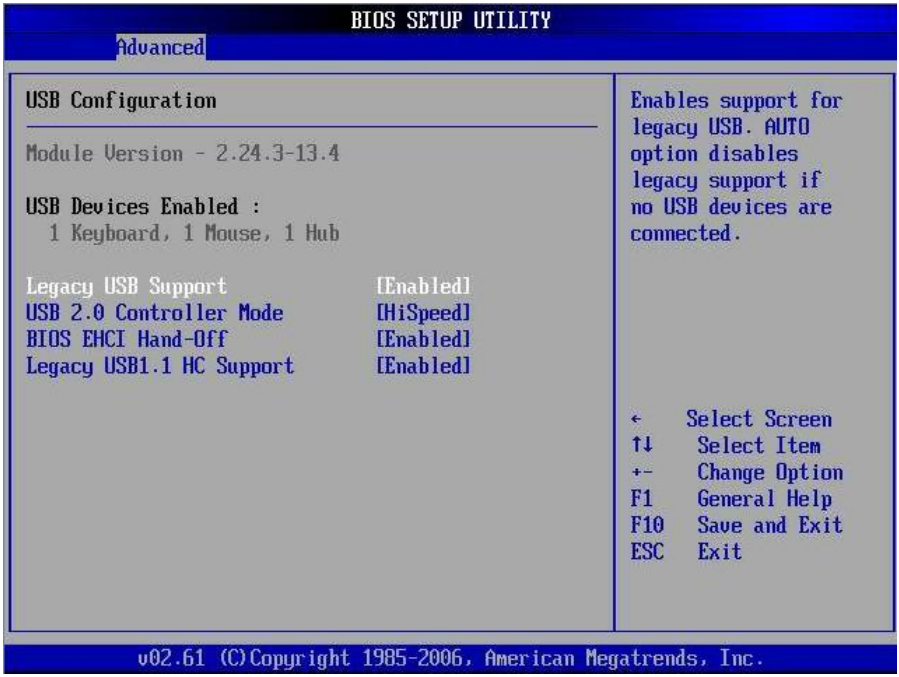
Redirection After BIOS POST
Disabled: Turns off the redirection after POST Boot Loader;
Boot Loader: Redirection is active during POST and during Boot Loader;
Always: Redirection is always active. (Some OSs may not work if set to Always)
Terminal Type
Select the target terminal type.

ANSI / VT100 / VT-UTF8

VT-UTF8 Combo Key Support
Enable VT-UTF8 Combination Key Support for ANSI/VT100 terminals.
Disabled / **Enable**

Sredir Memory Display Delay
Gives the delay in seconds to display memory information.
No Delay / Delay 1 Sec / Delay 2 Sec / Delay 4 Sec

3.3.11 USB Configuration


BIOS SETUP UTILITY
Advanced
USB Configuration
Module Version - 2.24.3-13.4
USB Devices Enabled :
1 Keyboard, 1 Mouse, 1 Hub
Legacy USB Support [Enabled]
USB 2.0 Controller Mode [HiSpeed]
BIOS EHCI Hand-Off [Enabled]
Legacy USB1.1 HC Support [Enabled]
Enables support for legacy USB. AUTO option disables legacy support if no USB devices are connected.
← Select Screen
↑↓ Select Item
+- Change Option
F1 General Help
F10 Save and Exit
ESC Exit
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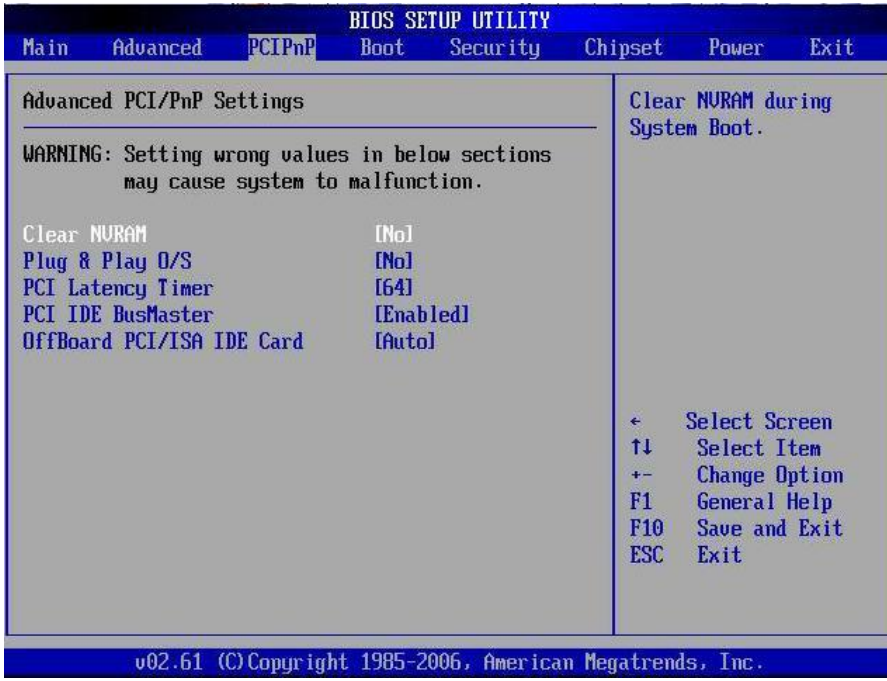
Legacy USB Support
Enables support for legacy USB. AUTO option disables legacy support if no USB devices are connected.
Disabled / Enabled / Auto

USB 2.0 Controller Mode
Configure the USB 2.0 controller in Hi Speed (480 Mbps) or Full Speed (12Mbps).
Hi Speed / Full Speed

BIOS EHCI Hand-OFF
This is a work around for OSes without EHCI hand-off support. The EHCI ownership change should claim by EHCI driver.
Enabled / Disabled

Legacy USB1.1 HC Support
Enables support for legacy USB. AUTO option disables legacy support if no USB devices are connected.
Enabled / Disabled

3.4 PCI/PnP


BIOS SETUP UTILITY
Main Advanced PCIPnP Boot Security Chipset Power Exit
Advanced PCI/PnP Settings
WARNING: Setting wrong values in below sections may cause system to malfunction.
Clear NVRAM [No]
Plug & Play O/S [No]
PCI Latency Timer [64]
PCI IDE BusMaster [Enabled]
OffBoard PCI/ISA IDE Card [Auto]
Clear NVRAM during System Boot.
← Select Screen
↑↓ Select Item
+- Change Option
F1 General Help
F10 Save and Exit
ESC Exit
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Clear NVRAM
Clears NVRAM during system Boot.
No / Yes

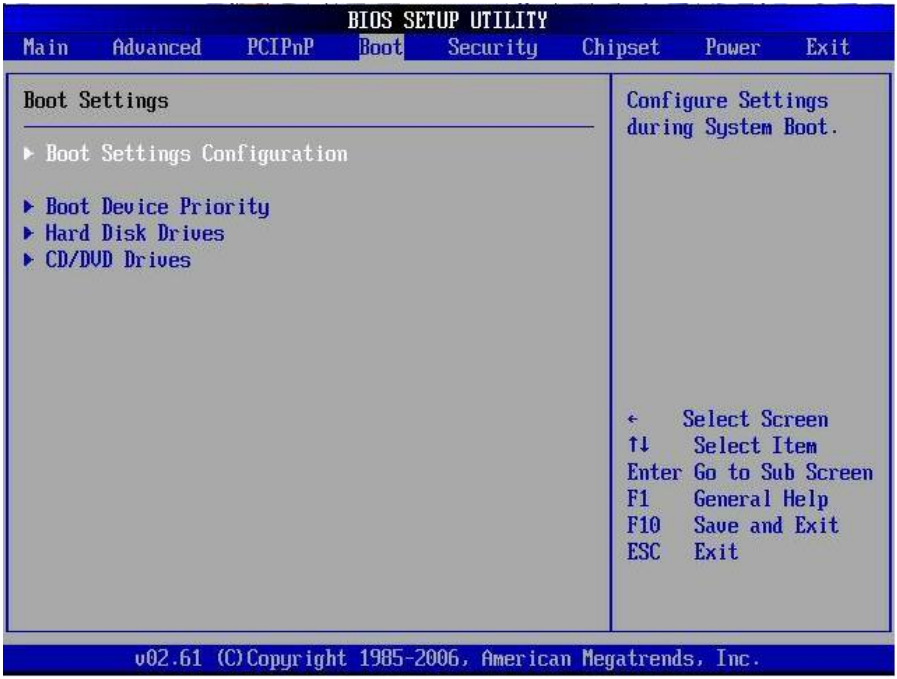
Plug & Play O/S
No: lets the BIOS configure all the devices in the system.
Yes: lets the operating system configure Plug and Play (PnP) devices not required for boot if your system has a Plug and Play operating system.
No / Yes

PCI Latency Timer
This setting controls how many PCI clocks each PCI device can hold the bus before another PCI device takes over. When set to higher values, every PCI device can conduct transactions for a longer time and thus improve the effective PCI bandwidth.
Values in units of PCI clocks for PCI device latency timer register
64 / 32 / 96 / 128 / 160 / 192 / 224 / 248

PCI IDE Bus Master
Enabled: BIOS uses PCI bus mastering for reading / writing to IDE drives.
Enabled / Disabled

OffBoard PCI/ISA IDE Card
Some PCI IDE cards may require this to be set to the PCI slot number that is holding the card.
AUTO: Works for most PCI IDE cards.
Auto / PCI Slot1 / PCI Slot2 / PCI Slot3 / PCI Slot4 / PCI Slot5 / PCI Slot6

3.5 Boot



Boot Settings Configuration

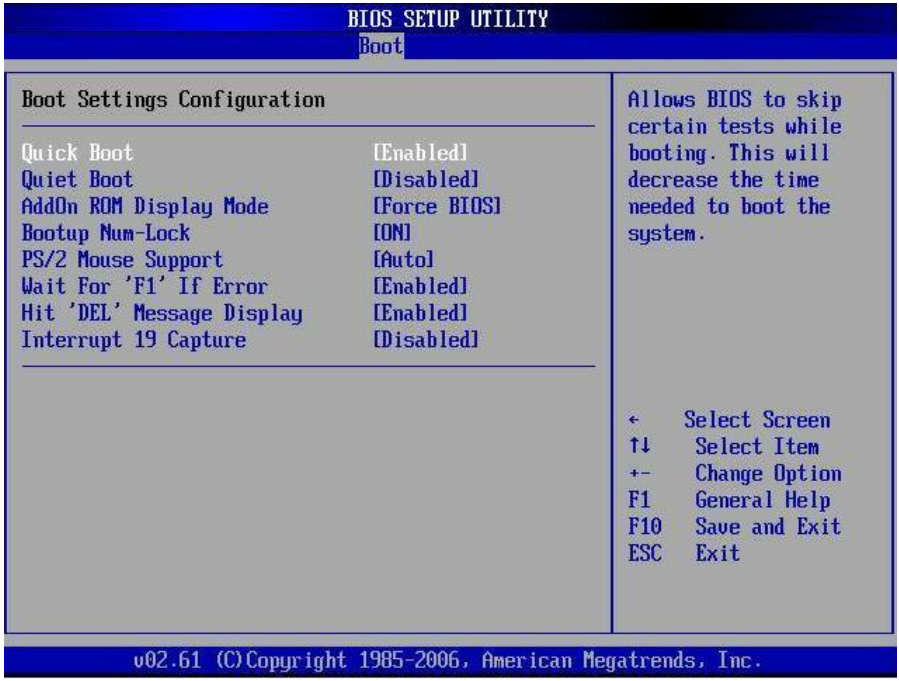
Configure Settings during System Boot.

Boot Device Priority
Specifies the Boot Device Priority sequence.

Removable Drives
Specifies the Boot Device Priority sequence from available Removable Drives.

CD/DVD Drives
Specifies the Boot Device Priority sequence from available CD/DVD Drives.

3.5.1 Boot Settings Configuration


BIOS SETUP UTILITY
Boot
Boot Settings Configuration
Quick Boot [Enabled]
Quiet Boot [Disabled]
AddOn ROM Display Mode [Force BIOS]
Bootup Num-Lock [ON]
PS/2 Mouse Support [Auto]
Wait For 'F1' If Error [Enabled]
Hit 'DEL' Message Display [Enabled]
Interrupt 19 Capture [Disabled]
Allows BIOS to skip certain tests while booting. This will decrease the time needed to boot the system.
← Select Screen
↑↓ Select Item
+- Change Option
F1 General Help
F10 Save and Exit
ESC Exit
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Quick Boot
This option allows user bypass BIOS self test during POST.
Enabled / Disabled

Quiet Boot
Disabled: displays normal POST messages.
Enabled: displays OEM log instead of POST messages.
Disabled / Enabled

Add On ROM Display Mode
Allows user to force BIOS/Option ROM of add-on cards to be displayed during quiet boot.
Force BIOS / Keep Current

Boot Up Num-Lock
Selects Power-on state for Numlock.
On / Off

PS/2 Mouse Support
Select support for PS/2 Mouse.
Auto / Enabled / Disabled

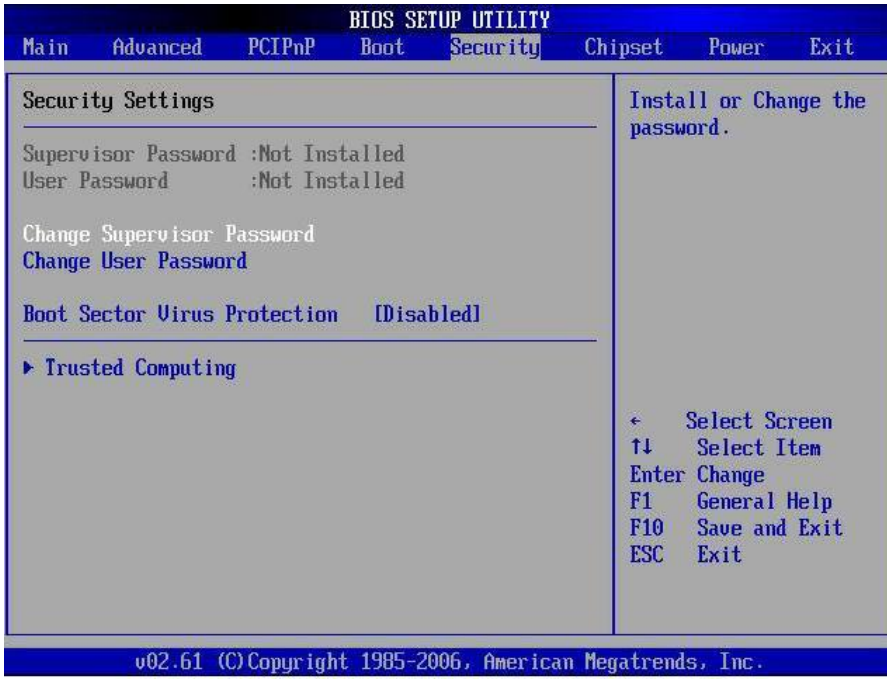
Wait for 'F1' If Error

Waits for F1 key to be present if error occurs.
Enabled / Disabled

Hit 'DEL' Message Display
Displays "Press DEL to run Setup in POST".
Enabled / Disabled

Interrupt 19 Capture
Enabled: allows option ROMs to trap interrupt 19.
Enabled / Disabled

3.6 Security



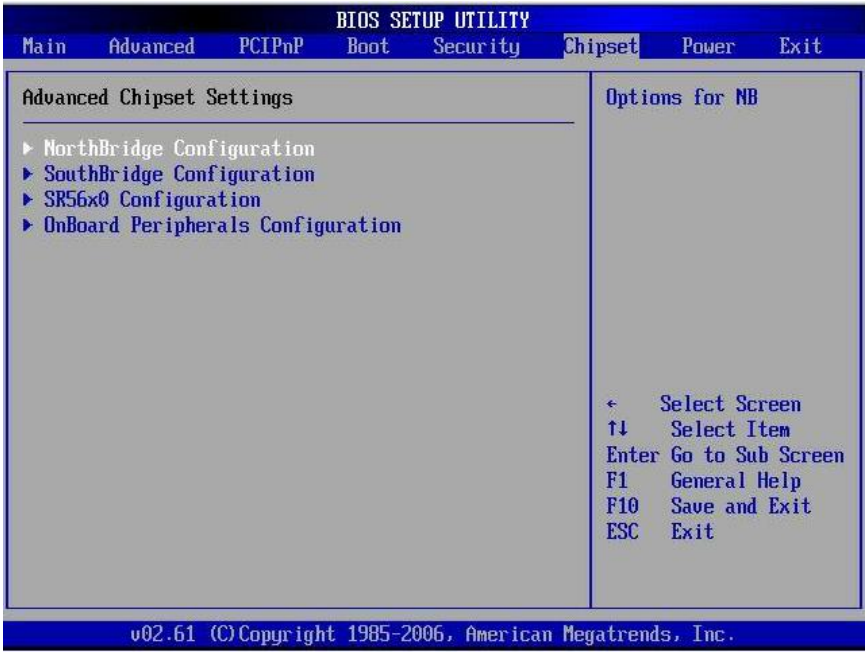
Change Supervisor Password
Install or Change the password.

Change User Password
Install or Change the password.

Boot Sector Virus Protection
When it is set to [Enabled], BIOS will issue a virus warning message and beep if a write to the boot sector or the partition table of the HDD is attempted.
Disabled / Enabled

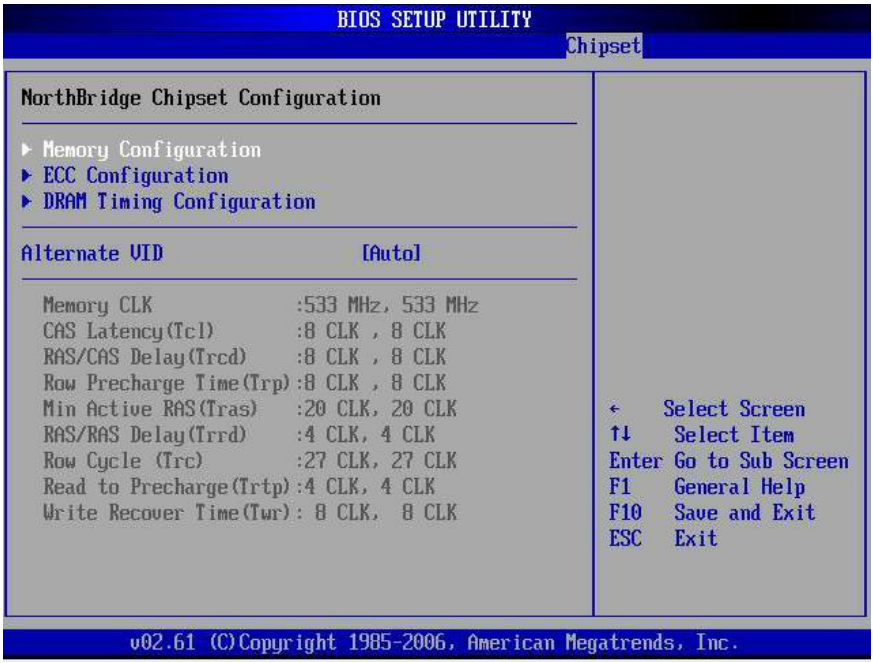
Trusted Computing
Configure settings related to Trusted Computing innovations.
To Enable / Disable TPM TCG (TPM 1.1/1.2) support in BIOS.
No / Yes

3.7 Chipset



Allows you to change NorthBridge, SouthBridge, or OnBoard Peripherals Configuration

3.7.1 North Bridge Configuration


BIOS SETUP UTILITY
Chipset
NorthBridge Chipset Configuration
Memory Configuration
ECC Configuration
DRAM Timing Configuration
Alternate VID [Auto]
Memory CLK :533 MHz, 533 MHz
CAS Latency(Tcl) :8 CLK , 8 CLK
RAS/CAS Delay(Trcd) :8 CLK , 8 CLK
Row Precharge Time(Trp) :8 CLK , 8 CLK
Min Active RAS(Tras) :20 CLK, 20 CLK
RAS/RAS Delay(Trrd) :4 CLK, 4 CLK
Row Cycle (Trc) :27 CLK, 27 CLK
Read to Precharge(Trtp) :4 CLK, 4 CLK
Write Recover Time(Twr) : 8 CLK, 8 CLK
← Select Screen
↑↓ Select Item
Enter Go to Sub Screen
F1 General Help
F10 Save and Exit
ESC Exit
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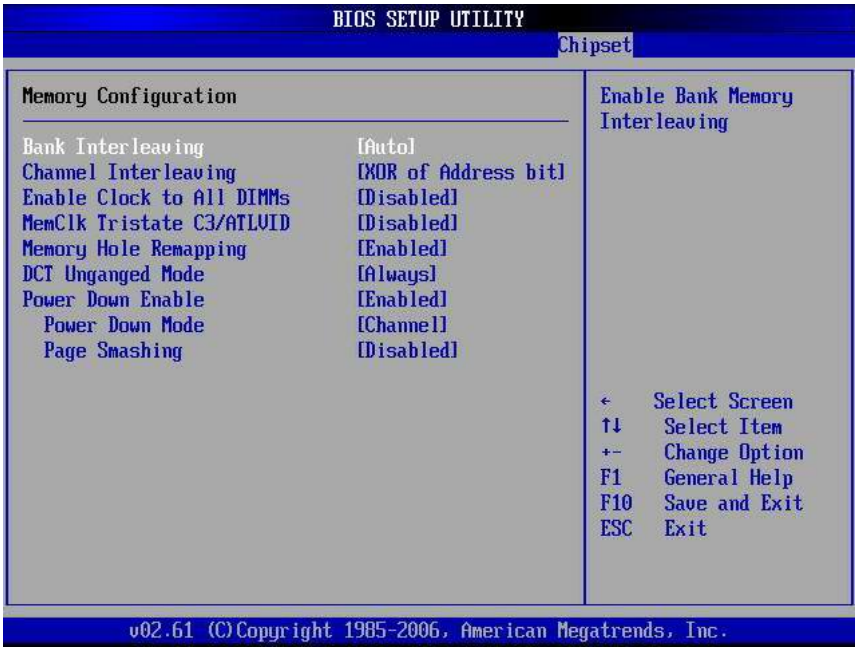
Alternate VID
Specify the alternate VID while in low power states.

3.7.1.1 Memory Configuration



Bank Interleaving
Enable or Disable Bank Memory Interleaving.
Auto / Disabled

Channel Interleaving
Enable or Disable Channel Memory Interleaving.
Disabled / Address bits 6 / Address bits 12 / **XOR of Address bits [20:16, 6]** / XOR of Address bits [20:16, 9]

Enable Clock to All DIMMs
Enable unused clocks to DIMMs even memory slots are not populated.
Disabled / Enabled

MemClk Tristate C3/ATLVID
Enable or Disable MemClk Tri-Stating during C3 and Alt VID.
Disabled / Enabled

Memory Hole Remapping
Enable or Disable Memory Hold remapping.
Enabled / Disabled

DCT Unganged Mode

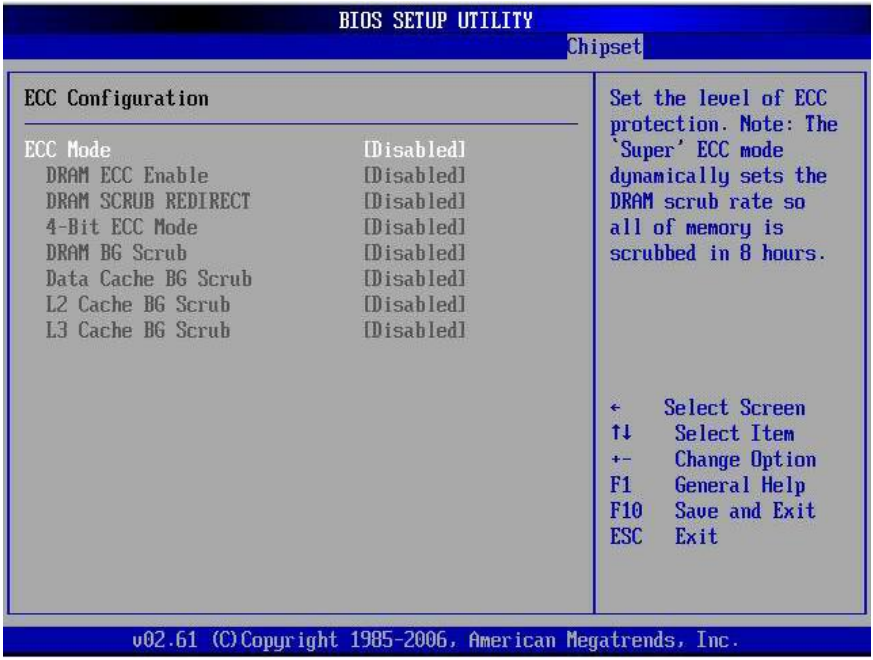
This allows selection of unganged DRAM mode (64-bit width).
Auto = Ganged mode
Always = Unganged mode

Power Down Enable
Enable or Disable DDR power down mode.
Enabled / Disabled

Power Down Mode
Set the DDR power down mode.
Channel / Chip select

Page Smashing
S/W Control of Page smashing mechanism
Disabled / IC / DC / Both

3.7.1.2 ECC Configuration


BIOS SETUP UTILITY
Chipset
ECC Configuration
ECC Mode [Disabled]
DRAM ECC Enable [Disabled]
DRAM SCRUB REDIRECT [Disabled]
4-Bit ECC Mode [Disabled]
DRAM BG Scrub [Disabled]
Data Cache BG Scrub [Disabled]
L2 Cache BG Scrub [Disabled]
L3 Cache BG Scrub [Disabled]
Set the level of ECC protection. Note: The 'Super' ECC mode dynamically sets the DRAM scrub rate so all of memory is scrubbed in 8 hours.
← Select Screen
↑↓ Select Item
+- Change Option
F1 General Help
F10 Save and Exit
ESC Exit
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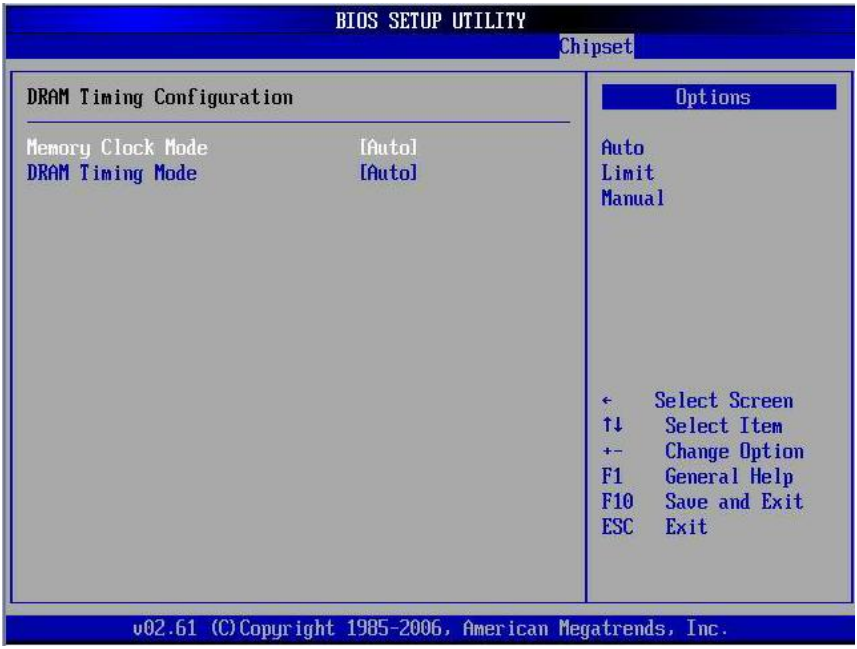
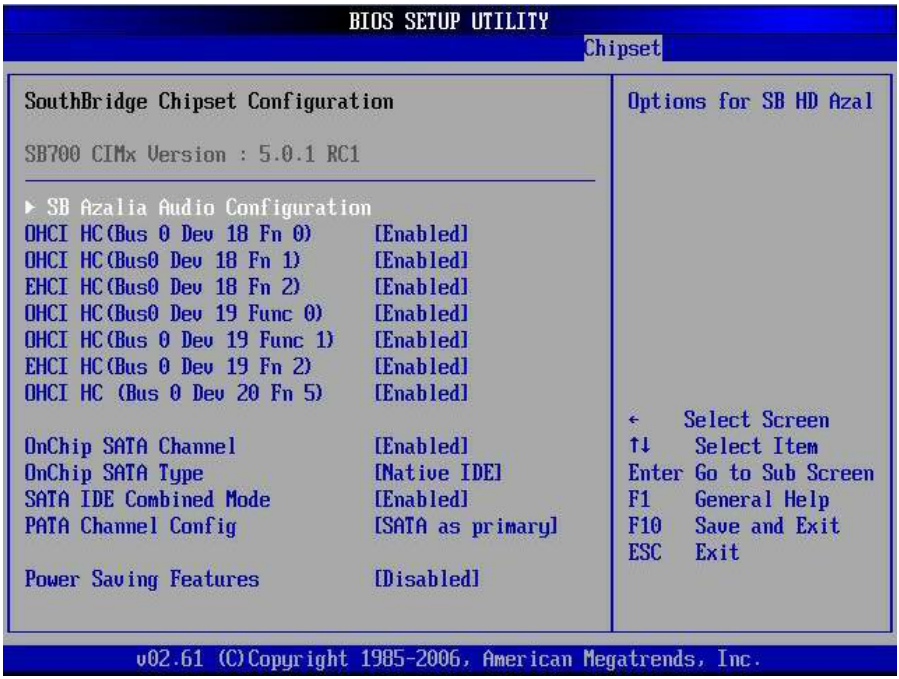
Set the level of ECC protection.

3.7.1.3 DRAM Timing Configuration



Set Memory Clock Mode and Dram Timing Mode.

3.7.2 South Bridge Configuration


BIOS SETUP UTILITY
Chipset
SouthBridge Chipset Configuration
SB700 CIMx Version : 5.0.1 RC1
▶ SB Azalia Audio Configuration
OHCI HC(Bus 0 Dev 18 Fn 0) [Enabled]
OHCI HC(Bus0 Dev 18 Fn 1) [Enabled]
EHCI HC(Bus0 Dev 18 Fn 2) [Enabled]
OHCI HC(Bus0 Dev 19 Func 0) [Enabled]
OHCI HC(Bus 0 Dev 19 Func 1) [Enabled]
EHCI HC(Bus 0 Dev 19 Fn 2) [Enabled]
OHCI HC (Bus 0 Dev 20 Fn 5) [Enabled]
OnChip SATA Channel [Enabled]
OnChip SATA Type [Native IDE]
SATA IDE Combined Mode [Enabled]
PATA Channel Config [SATA as primary]
Power Saving Features [Disabled]
Options for SB HD Azal
← Select Screen
↑↓ Select Item
Enter Go to Sub Screen
F1 General Help
F10 Save and Exit
ESC Exit
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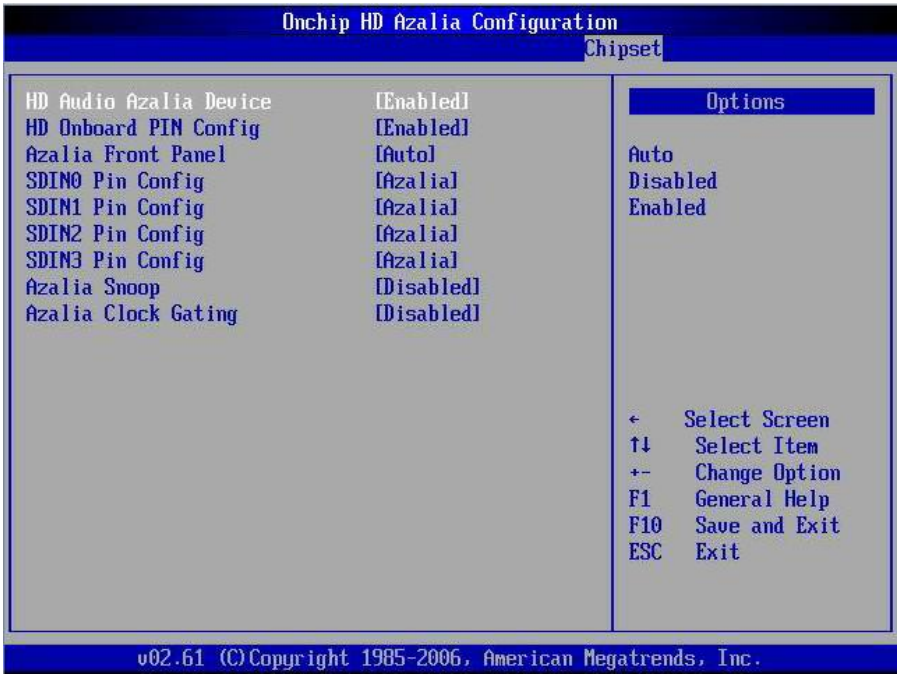
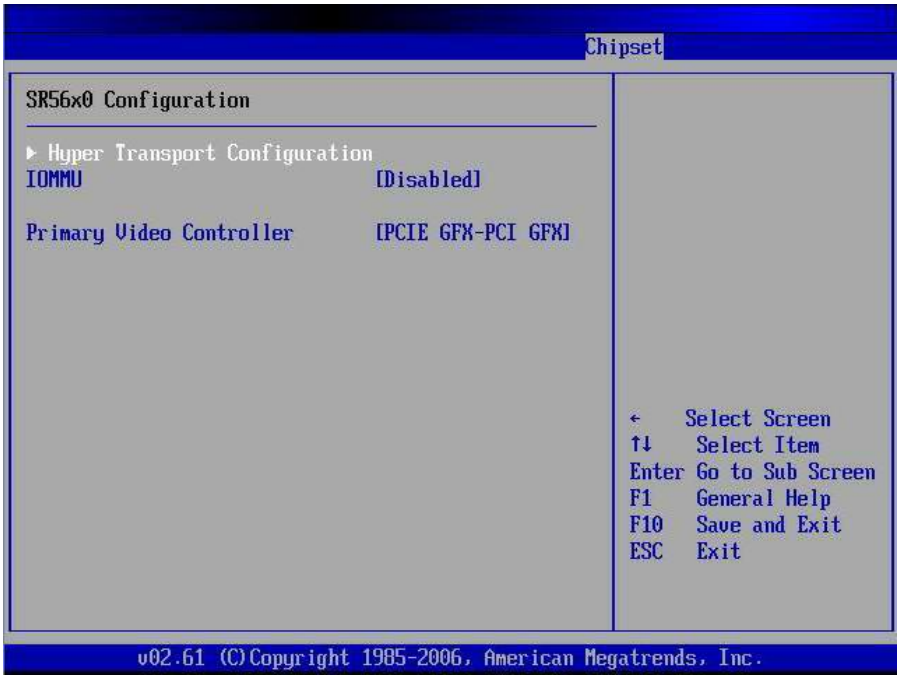
OnChip SATA Type
Native IDE: Use IO address assigned by PCI base register as IDE port;
RAID;
AHCI;
Legacy IDE: Use legacy IDE control port.

Power Saving Features
Disable or Enable power saving features in SB. As general rule, this feature should be disabled for desktop and enabled for mobile. See AMD SB700 Power Saving document for more details.
Disabled / Enabled

3.7.2.1 DRAM Timing Configuration



3.7.3 SR56x0 Configuration

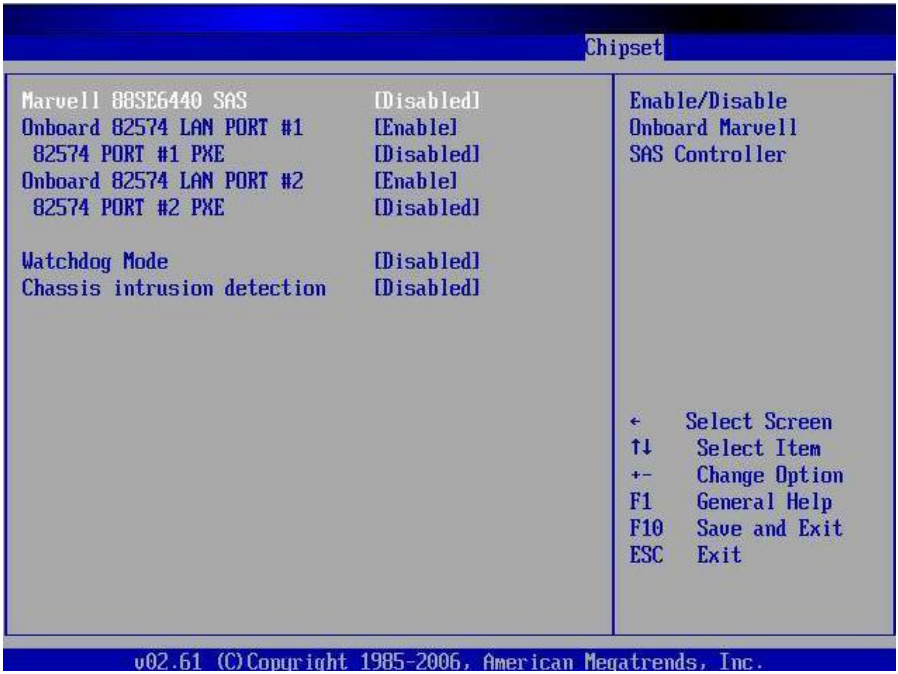


IOMMU
Disable or Enable IOMMU function.
Disabled / Enable

Primary Video Controller
PCIE GFX-PCI GFX: Video card scan from PCIE bus to PCI bus.
PCI GFX-PCIE GFX: Video card scan from PCI bus (onboard VGA) to PCIE bus.

3.7.4 Onboard Peripherals Configuration

Use this section to enable or disable onboard devices.

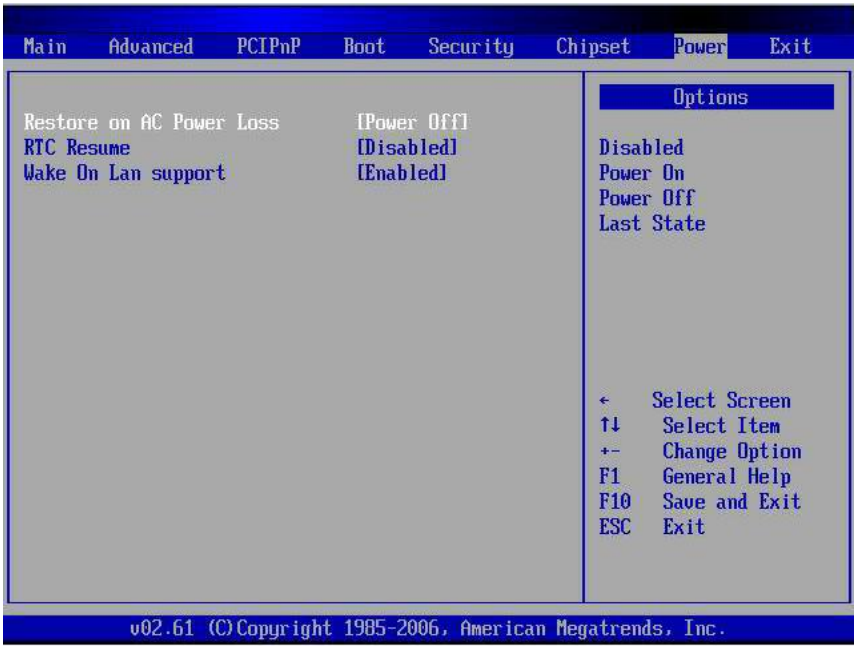


Marvell 88SE6440 SAS
Enable or Disable Onboard Marvell SAS controller.

Watchdog Mode
Disabled: Disable Watchdog;
POST: BIOS POST Watchdog, timer counting starts at PowerOn, stops at OS boot;
OS: Boot watchdog, starts at OS boot;
PowerOn: Start at PowerOn.

Chassis intrusion detection
Disabled: Disable Chassis intrusion detection;
Enabled: When a chassis open event is detected, the BIOS will record the event and issue a warning beep.

3.8 Power

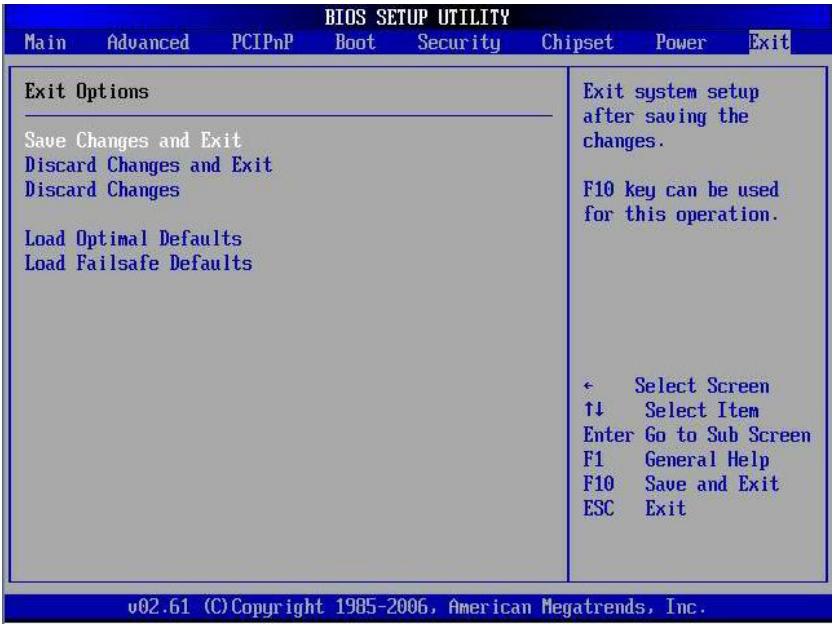


Restore on AC Power Loss
Disabled / Power On / **Power Off** / Last State

RTC Resume
Disable / Enable RTC to generate a wake event.
Disabled / Enabled

Wake On Lan support
Enable or Disable Wke On Lan Function in non-ACPI OS.
Disabled / **Enabled**

3.9 Exit


BIOS SETUP UTILITY
Main Advanced PCIPnP Boot Security Chipset Power Exit
Exit Options
Save Changes and Exit
Discard Changes and Exit
Discard Changes
Load Optimal Defaults
Load Failsafe Defaults
Exit system setup after saving the changes.
F10 key can be used for this operation.
← Select Screen
↑↓ Select Item
Enter Go to Sub Screen
F1 General Help
F10 Save and Exit
ESC Exit
v02.61 (C)Copyright 1985-2006, American Megatrends, Inc.

Save Changes and Exit
Use this option to exit setup utility and re-boot.
All new selections you have made are stored into CMOS.
System will use the new settings to boot up.

Discard Changes and Exit
Use this option to exit setup utility and re-boot.
All new selections you have made are not stored into CMOS.
System will use the old settings to boot up.

Discard Changes
Use this option to restore all new setup values that you have made but not saved into CMOS.

Load Optimal Defaults
Use this option to load default performance setup values.
Use this option when system CMOS values have been corrupted or modified incorrectly.

Load Failsafe Defaults
Use this option to load all default failsafe setup values.
Use this option when troubleshooting.

Chapter 4: Diagnostics

NOTE: if you experience problems with setting up your system, always check the following things in the following order:

Memory, Video, CPU

By checking these items, you will most likely find out what the problem might have been when setting up your system. For more information on troubleshooting, check the TYAN website at http://www.tyan.com.

4.1 Beep Codes

Fatal errors, which halt the boot process, are communicated through two kinds of audible beeps.
- Eight short beeps: It indicates that a video error has occurred.
- A single long beep repeatedly: It indicates that a DRAM error has occurred.

The most common type of error is a memory error.

Before contacting your vendor or TYAN Technical Support, be sure that you note as much as you can about the beep code length and order that you experience. Also, be ready with information regarding add-in cards, drives and O/S to speed the support process and come to a quicker solution.

4.2 Flash Utility

Every BIOS file is unique for the motherboard it was designed for. For Flash Utilities, BIOS downloads, and information on how to properly use the Flash Utility with your motherboard, please check the TYAN web site at http://www.tyan.com

NOTE: Please be aware that by flashing your BIOS, you agree that in the event of a BIOS flash failure, you must contact your dealer for a replacement BIOS. There are no exceptions. TYAN does not have a policy for replacing BIOS chips directly with end users. In no event will TYAN be held responsible for damages done by the end user.

4.3 AMIBIOS Post Code

The POST code checkpoints are the largest set of checkpoints during the BIOS pre-boot process. The following table describes the type of checkpoints that may occur during the POST portion of the BIOS:

Checkpoint	Description
03	Disable NMI, Parity, video for EGA, and DMA controllers. Initialize BIOS, POST, Runtime data area. Also initialize BIOS modules on POST entry and GPNV area. Initialized CMOS as mentioned in the Kernel Variable "wCMOSFlags."
04	Check CMOS diagnostic byte to determine if battery power is OK and CMOS checksum is OK. Verify CMOS checksum manually by reading storage area. If the CMOS checksum is bad, update CMOS with power-on default values and clear passwords. Initialize status register A. Initializes data variables that are based on CMOS setup questions. Initializes both the 8259 compatible PICs in the system
05	Initializes the interrupt controlling hardware (generally PIC) and interrupt vector table.
06	Do R/W test to CH-2 count reg. Initialize CH-0 as system timer.Install the POSTINT1Ch handler. Enable IRQ-0 in PIC for system timer interrupt. Traps INT1Ch vector to "POSTINT1ChHandlerBlock."
08	Initializes the CPU. The BAT test is being done on KBC. Program the keyboard controller command byte is being done after Auto detection of KB/MS using AMI KB-5.
0A	Initializes the 8042 compatible Key Board Controller.
0B	Detects the presence of PS/2 mouse.
0C	Detects the presence of Keyboard in KBC port.
0E	Testing and initialization of different Input Devices. Also, update the Kernel Variables. Traps the INT09h vector, so that the POST INT09h handler gets control for IRQ1. Uncompress all available language, BIOS logo, and Silent logo modules.
13	Early POST initialization of chipset registers.
24	Uncompress and initialize any platform specific BIOS modules.
30	Initialize System Management Interrupt.
2A	Initializes different devices through DIM. See *DIM Code Checkpoints* section of document for more information.
2C	Initializes different devices. Detects and initializes the video adapter installed in the system that have optional ROMs.
2E	Initializes all the output devices.
31	Allocate memory for ADM module and uncompress it. Give control to ADM module for initialization. Initialize language and font modules for ADM. Activate ADM module.
33	Initializes the silent boot module. Set the window for displaying text information.

Checkpoint	Description
37	Displaying sign-on message, CPU information, setup key message, and any OEM specific information.
38	Initializes different devices through DIM. See *DIM Code Checkpoints* section of document for more information.
39	Initializes DMAC-1 & DMAC-2.
3A	Initialize RTC date/time.
3B	Test for total memory installed in the system. Also, Check for DEL or ESC keys to limit memory test. Display total memory in the system.
3C	Mid POST initialization of chipset registers.
40	Detect different devices (Parallel ports, serial ports, and coprocessor in CPU, … etc.) successfully installed in the system and update the BDA, EBDA…etc.
50	Programming the memory hole or any kind of implementation that needs an adjustment in system RAM size if needed.
52	Updates CMOS memory size from memory found in memory test. Allocates memory for Extended BIOS Data Area from base memory.
60	Initializes NUM-LOCK status and programs the KBD typematic rate.
75	Initialize Int-13 and prepare for IPL detection.
78	Initializes IPL devices controlled by BIOS and option ROMs.
7A	Initializes remaining option ROMs.
7C	Generate and write contents of ESCD in NVRam.
84	Log errors encountered during POST.
85	Display errors to the user and gets the user response for error.
87	Execute BIOS setup if needed / requested.
8C	Late POST initialization of chipset registers.
8E	Program the peripheral parameters. Enable/Disable NMI as selected
90	Late POST initialization of system management interrupt.
A0	Check boot password if installed.
A1	Clean-up work needed before booting to OS.
A2	Takes care of runtime image preparation for different BIOS modules. Fill the free area in F000h segment with 0FFh. Initializes the Microsoft IRQ Routing Table. Prepares the runtime language module. Disables the system configuration display if needed.
A4	Initialize runtime language module.
A7	Displays the system configuration screen if enabled. Initialize the CPU's before boot, which includes the programming of the MTRR's.
A8	Prepare CPU for OS boot including final MTRR values.
A9	Wait for user input at config display if needed.
AA	Uninstall POST INT1Ch vector and INT09h vector. Deinitializes the ADM module.
AB	Prepare BBS for Int 19 boot.
AC	End of POST initialization of chipset registers.
B1	Save system context for ACPI.
00	Passes control to OS Loader (typically INT19h).

Glossary

ACPI (Advanced Configuration and Power Interface): a power management specification that allows the operating system to control the amount of power distributed to the computer's devices. Devices not in use can be turned off, reducing unnecessary power expenditure.

AGP (Accelerated Graphics Port): a PCI-based interface which was designed specifically for demands of 3D graphics applications. The 32-bit AGP channel directly links the graphics controller to the main memory. While the channel runs only at 66 MHz, it supports data transmission during both the rising and falling ends of the clock cycle, yielding an effective speed of 133 MHz.

ATAPI (AT Attachment Packet Interface): also known as IDE or ATA; a drive implementation that includes the disk controller on the device itself. It allows CD-ROMs and tape drives to be configured as master or slave devices, just like HDDs.

ATX: the form factor designed to replace the AT form factor. It improves on the AT design by rotating the board 90 degrees, so that the IDE connectors are closer to the drive bays, and the CPU is closer to the power supply and cooling fan. The keyboard, mouse, USB, serial, and parallel ports are built-in.

Bandwidth: refers to carrying capacity. The greater the bandwidth, the more data the bus, phone line, or other electrical path can carry. Greater bandwidth results in greater speed.

BBS (BIOS Boot Specification): a feature within the BIOS that creates, prioritizes, and maintains a list of all Initial Program Load (IPL) devices, and then stores that list in NVRAM. IPL devices have the ability to load and execute an OS, as well as provide the ability to return to the BIOS if the OS load process fails. At that point, the next IPL device is called upon to attempt loading of the OS.

BIOS (Basic Input/Output System): the program that resides in the ROM chip, which provides the basic instructions for controlling your computer's hardware. Both the operating system and application software use BIOS routines to ensure compatibility.

Buffer: a portion of RAM which is used to temporarily store data; usually from an application though it is also used when printing and in most keyboard drivers. The CPU can manipulate data in a buffer before copying it to a disk drive. While this improves system performance (reading to or writing from a disk drive a single time is much faster than doing so repeatedly) there is the possibility of

losing your data should the system crash. Information in a buffer is temporarily stored, not permanently saved.

Bus: a data pathway. The term is used especially to refer to the connection between the processor and system memory, and between the processor and PCI or ISA local buses.

Bus mastering: allows peripheral devices and IDEs to access the system memory without going through the CPU (similar to DMA channels).

Cache: a temporary storage area for data that will be needed often by an application. Using a cache lowers data access times since the information is stored in SRAM instead of slower DRAM. Note that the cache is also much smaller than your regular memory: a typical cache size is 512KB, while you may have as much as 4GB of regular memory.

Closed and open jumpers: jumpers and jumper pins are active when they are “on” or “closed”, and inactive when they are “off” or “open”.

CMOS (Complementary Metal-Oxide Semiconductors): chips that hold the basic startup information for the BIOS.

COM port: another name for the serial port, which is called as such because it transmits the eight bits of a byte of data along one wire, and receives data on another single wire (that is, the data is transmitted in serial form, one bit after another). Parallel ports transmit the bits of a byte on eight different wires at the same time (that is, in parallel form, eight bits at the same time).

DDR (Double Data Rate): a technology designed to double the clock speed of the memory. It activates output on both the rising and falling edge of the system clock rather than on just the rising edge, potentially doubling output.

DIMM (Dual In-line Memory Module): faster and more capacious form of RAM than SIMMs, and do not need to be installed in pairs.

DIMM bank: sometimes called DIMM socket because the physical slot and the logical unit are the same. That is, one DIMM module fits into one DIMM socket, which is capable of acting as a memory bank.

DMA (Direct Memory Access): channels that are similar to IRQs. DMA channels allow hardware devices (like soundcards or keyboards) to access the main memory without involving the CPU. This frees up CPU resources for other tasks. As with IRQs, it is vital that you do not double up devices on a single line. Plug-n-Play devices will take care of this for you.

DRAM (Dynamic RAM): widely available, very affordable form of RAM which looses data if it is not recharged regularly (every few milliseconds). This refresh requirement makes DRAM three to ten times slower than non-recharged RAM such as SRAM.

ECC (Error Correction Code or Error Checking and Correcting): allows data to be checked for errors during run-time. Errors can subsequently be corrected at the same time that they're found.

EEPROM (Electrically Erasable Programmable ROM): also called Flash BIOS, it is a ROM chip which can, unlike normal ROM, be updated. This allows you to keep up with changes in the BIOS programs without having to buy a new chip. TYAN®'s BIOS updates can be found at http://**www.tyan.com**

ESCD (Extended System Configuration Data): a format for storing information about Plug-n-Play devices in the system BIOS. This information helps properly configure the system each time it boots.

Firmware: low-level software that controls the system hardware.

Form factor: an industry term for the size, shape, power supply type, and external connector type of the Personal Computer Board (PCB) or motherboard. The standard form factors are the AT and ATX.

Global timer: onboard hardware timer, such as the Real-Time Clock (RTC).

HDD: stands for Hard Disk Drive, a type of fixed drive.

H-SYNC: controls the horizontal synchronization/properties of the monitor.

HyperTransport™: a high speed, low latency, scalable point-to-point link for interconnecting ICs on boards. It can be significantly faster than a PCI bus for an equivalent number of pins. It provides the bandwidth and flexibility critical for today's networking and computing platforms while retaining the fundamental programming model of PCI.

IC (Integrated Circuit): the formal name for the computer chip.

IDE (Integrated Device/Drive Electronics): a simple, self-contained HDD interface. It can handle drives up to 8.4 GB in size. Almost all IDEs sold now are in fact Enhanced IDEs (EIDEs), with maximum capacity determined by the hardware controller.

IDE INT (IDE Interrupt): Hardware interrupt signal that goes to the IDE.

I/O (Input/Output): the connection between your computer and another piece of hardware (mouse, keyboard, etc.)

IRQ (Interrupt Request): an electronic request that runs from a hardware device to the CPU. The interrupt controller assigns priorities to incoming requests and delivers them to the CPU. It is important that there is only one device hooked up to each IRQ line; doubling up devices on IRQ lines can lock up your system. Plug-n-Play operating systems can take care of these details for you.

Latency: the amount of time that one part of a system spends waiting for another part to catch up. This occurs most commonly when the system sends data out to a peripheral device and has to wait for the peripheral to spread (peripherals tend to be slower than onboard system components).

NVRAM: ROM and EEPROM are both examples of Non-Volatile RAM, memory that holds its data without power. DRAM, in contrast, is volatile.

Parallel port: transmits the bits of a byte on eight different wires at the same time.

PCI (Peripheral Component Interconnect): a 32 or 64-bit local bus (data pathway) which is faster than the ISA bus. Local buses are those which operate within a single system (as opposed to a network bus, which connects multiple systems).

PCI PIO (PCI Programmable Input/Output) modes: the data transfer modes used by IDE drives. These modes use the CPU for data transfer (in contrast, DMA channels do not). PCI refers to the type of bus used by these modes to communicate with the CPU.

PCI-to-PCI Bridge: allows you to connect multiple PCI devices onto one PCI slot.

Pipeline burst SRAM: a fast secondary cache. It is used as a secondary cache because SRAM is slower than SDRAM, but usually larger. Data is cached first to the faster primary cache, and then, when the primary cache is full, to the slower secondary cache.

PnP (Plug-n-Play): a design standard that has become ascendant in the industry. Plug-n-Play devices require little set-up to use. Devices and operating systems that are not Plug-n-Play require you to reconfigure your system each time you add or change any part of your hardware.

PXE (Preboot Execution Environment): one of four components that together make up the Wired for Management 2.0 baseline specification. PXE was

designed to define a standard set of preboot protocol services within a client with the goal of allowing networked-based booting to boot using industry standard protocols.

RAID (Redundant Array of Independent Disks): a way for the same data to be stored in different places on many hard drives. By using this method, the data is stored redundantly and multiple hard drives will appear as a single drive to the operating system. RAID level 0 is known as striping, where data is striped (or overlapped) across multiple hard drives, but offers no fault-tolerance. RAID level 1 is known as mirroring, which stores the data within at least two hard drives, but does not stripe. RAID level 1 also allows for faster access time and fault-tolerance, since either hard drive can be read at the same time. RAID level 0+1 is striping and mirroring, providing fault-tolerance, striping, and faster access all at the same time.

RAIDIOS: RAID I/O Steering (Intel)

RAM (Random Access Memory): technically refers to a type of memory where any byte can be accessed without touching the adjacent data and is often referred to the system's main memory. This memory is available to any program running on the computer.

ROM (Read-Only Memory): a storage chip which contains the BIOS; the basic instructions required to boot the computer and start up the operating system.

SDRAM (Synchronous Dynamic RAM): called as such because it can keep two sets of memory addresses open simultaneously. By transferring data alternately from one set of addresses and then the other, SDRAM cuts down on the delays associated with non-synchronous RAM, which must close one address bank before opening the next.

Serial port: called as such because it transmits the eight bits of a byte of data along one wire, and receives data on another single wire (that is, the data is transmitted in serial form, one bit after another).

SCSI Interrupt Steering Logic (SISL): Architecture that allows a RAID controller, such as AcceleRAID 150, 200 or 250, to implement RAID on a system board-embedded SCSI bus or a set of SCSI busses. SISL: SCSI Interrupt Steering Logic (LSI) (only on LSI SCSI boards)

Sleep/Suspend mode: in this mode, all devices except the CPU shut down.

SDRAM (Static RAM): unlike DRAM, this type of RAM does not need to be refreshed in order to prevent data loss. Thus, it is faster and more expensive.

SLI (Scalable Link Interface): NVIDIA SLI technology links two graphics cards together to provide scalability and increased performance. NVIDIA SLI takes

advantage of the increased bandwidth of the PCI Express bus architecture, and features hardware and software innovations within NVIDIA GPUs (graphics processing units) and NVIDIA MCPs (media and communications processors). Depending on the application, NVIDIA SLI can deliver as much as two times the performance of a single GPU configuration.

Standby mode: in this mode, the video and hard drives shut down; all other devices continue to operate normally.

UltraDMA-33/66/100: a fast version of the old DMA channel. UltraDMA is also called UltraATA. Without a proper UltraDMA controller, your system cannot take advantage of higher data transfer rates of the new UltraDMA/UltraATA hard drives.

USB (Universal Serial Bus): a versatile port. This one port type can function as a serial, parallel, mouse, keyboard or joystick port. It is fast enough to support video transfer, and is capable of supporting up to 127 daisy-chained peripheral devices.

VGA (Video Graphics Array): the PC video display standard

V-SYNC: controls the vertical scanning properties of the monitor.

ZCR (Zero Channel RAID): PCI card that allows a RAID card to use the onboard SCSI chip, thus lowering cost of RAID solution

ZIF Socket (Zero Insertion Force socket): these sockets make it possible to insert CPUs without damaging the sensitive CPU pins. The CPU is lightly placed in an open ZIF socket, and a lever is pulled down. This shifts the processor over and down, guiding it into the board and locking it into place.

Technical Support

If a problem arises with your system, you should first turn to your dealer for direct support. Your system has most likely been configured or designed by them and they should have the best idea of what hardware and software your system contains. Hence, they should be of the most assistance for you. Furthermore, if you purchased your system from a dealer near you, take the system to them directly to have it serviced instead of attempting to do so yourself (which can have expensive consequences).

If these options are not available for you then TYAN Computer Corporation can help. Besides designing innovative and quality products for over a decade, Tyan has continuously offered customers service beyond their expectations. TYAN's website (**www.tyan.com**) provides easy-to-access resources such as in-depth Linux Online Support sections with downloadable Linux drivers and comprehensive compatibility reports for chassis, memory and much more. With all these convenient resources just a few keystrokes away, users can easily find the latest software and operating system components to keep their systems running as powerful and productive as possible. TYAN also ranks high for its commitment to fast and friendly customer support through email. By offering plenty of options for users, TYAN serves multiple market segments with the industry's most competitive services to support them.

"TYAN's tech support is some of the most impressive we've seen, with great response time and exceptional organization in general" - Anandtech.com

You can contact TYAN Technical Support by using our Online Support System:

http://12.230.196.231/helpstar/hsPages/login.aspx?ReturnUrl=%2fhelpstar%2fhsPages%2fDefault.aspx

Help Resources:

1. See the beep codes section of this manual.
2. See the TYAN website for FAQ's, bulletins, driver updates, and other information: **http://www.tyan.com**
3. Contact your dealer for help BEFORE calling TYAN.
4. Check the TYAN user group: **alt.comp.periphs.mainboard.TYAN**

Returning Merchandise for Service

During the warranty period, contact your distributor or system vendor FIRST for any product problems. This warranty only covers normal customer use and does not cover damages incurred during shipping or failure due to the alteration, misuse, abuse, or improper maintenance of products.

NOTE: A receipt or copy of your invoice marked with the date of purchase is required before any warranty service can be rendered. You may obtain service by calling the manufacturer for a Return Merchandise Authorization (RMA) number. The RMA number should be prominently displayed on the outside of the shipping carton and the package should be mailed prepaid. TYAN will pay to have the board shipped back to you

Notice for the USA
Compliance Information Statement (Declaration of Conformity Procedure) DoC
FCC Part 15: This device complies with part 15 of the FCC Rules

Operation is subject to the following conditions:

This device may not cause harmful interference, and this device must accept any interference received including interference that may cause undesired operation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try one or more of the following measures:
Reorient or relocate the receiving antenna.
Increase the separation between the equipment and the receiver.
Plug the equipment into an outlet on a circuit different from that of the receiver.
Consult the dealer on an experienced radio/television technician for help.

Notice for Canada
This apparatus complies with the Class B limits for radio interference as specified in the Canadian Department of Communications Radio Interference Regulations. (Cet appareil est conforme aux norms de Classe B d'interference radio tel que specifie par le Ministere Canadien des Communications dans les reglements d'ineteference radio.)

CAUTION: Lithium battery included with this board. Do not puncture, mutilate, or dispose of battery in fire. Danger of explosion if battery is incorrectly replaced. Replace only with the same or equivalent type recommended by manufacturer. Dispose of used battery according to manufacturer instructions and in accordance with your local regulations.

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